

FIG. 1

Reader Network / Site

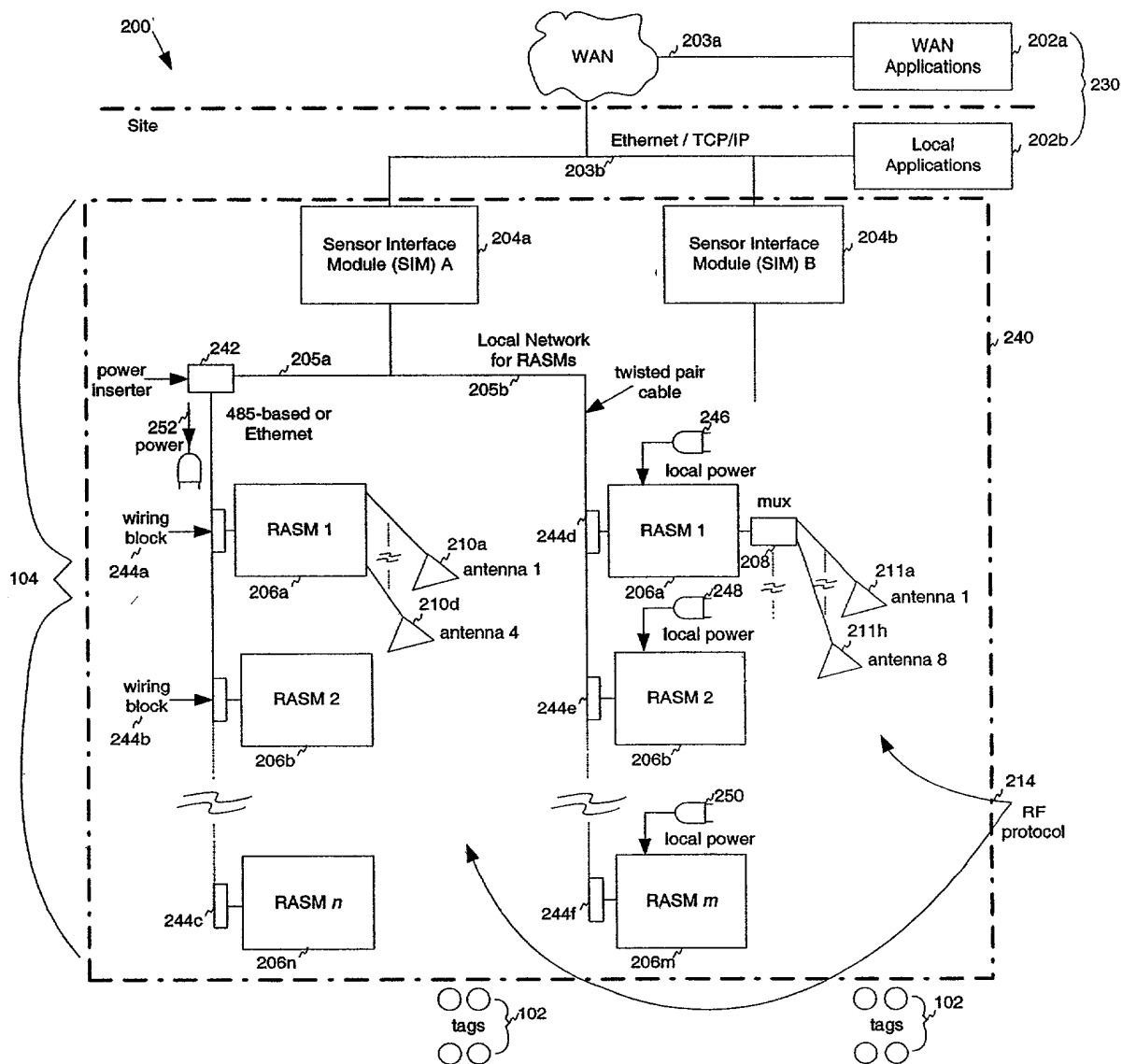


FIG. 2

Reader Data '0'

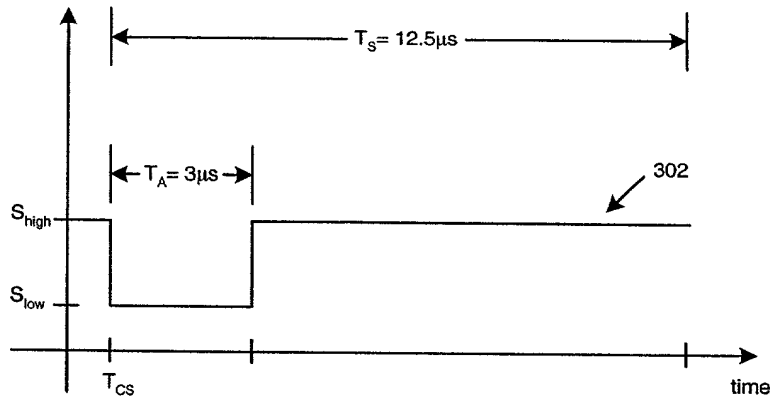


FIG. 3

Reader Data '1'

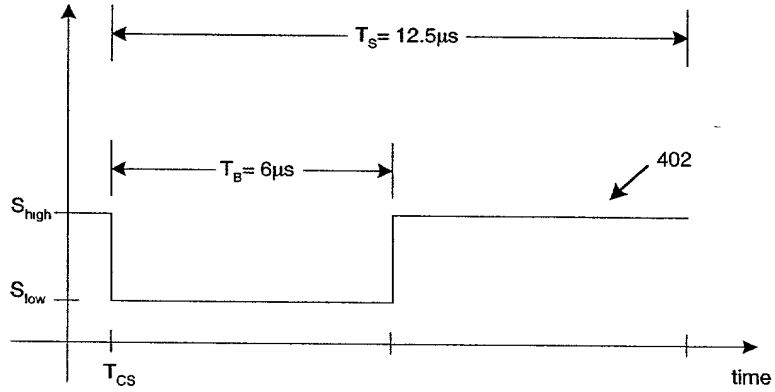


FIG. 4

Reader Data 'null'

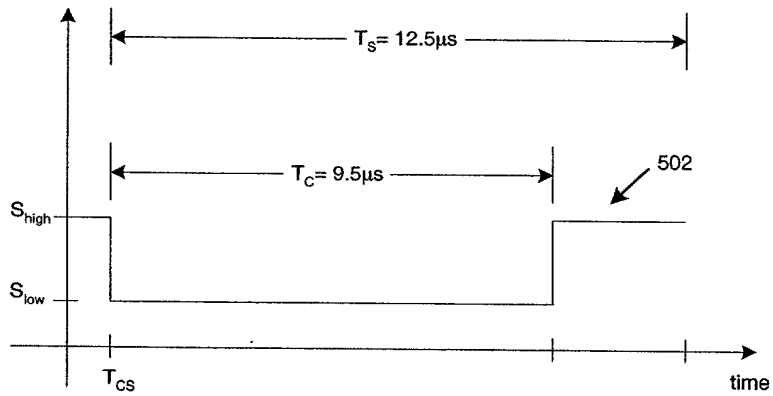


FIG. 5

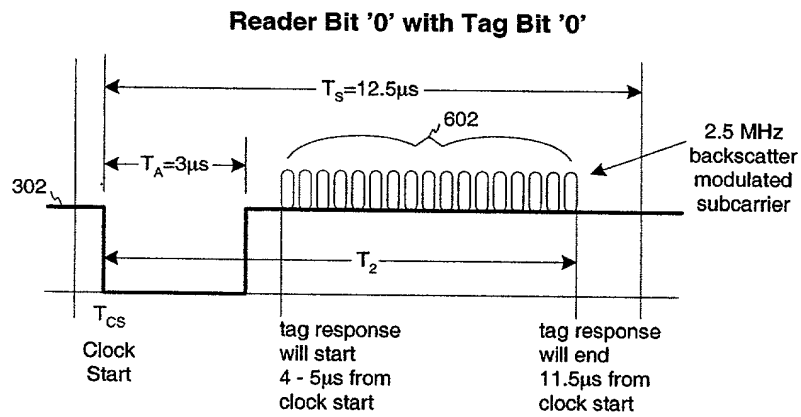


FIG. 6

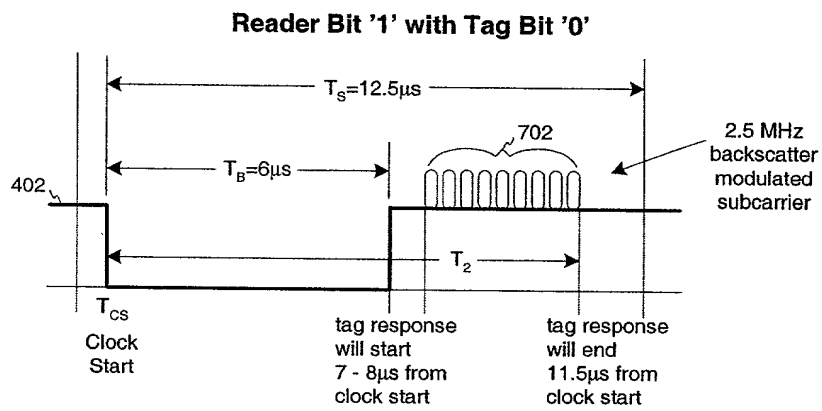


FIG. 7

Reader Bit '0' with Tag Bit '1'

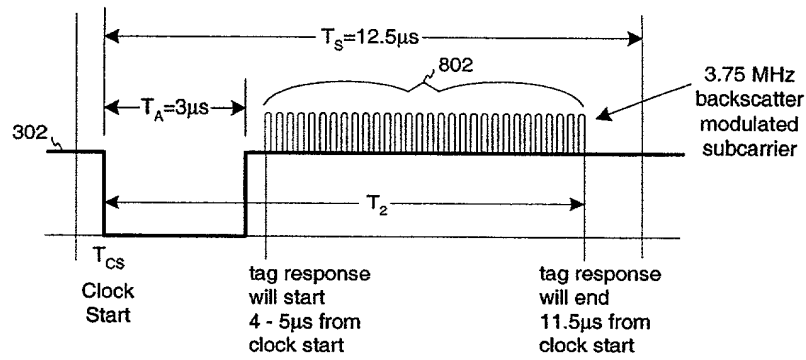


FIG. 8

Reader Bit '1' with Tag Bit '1'

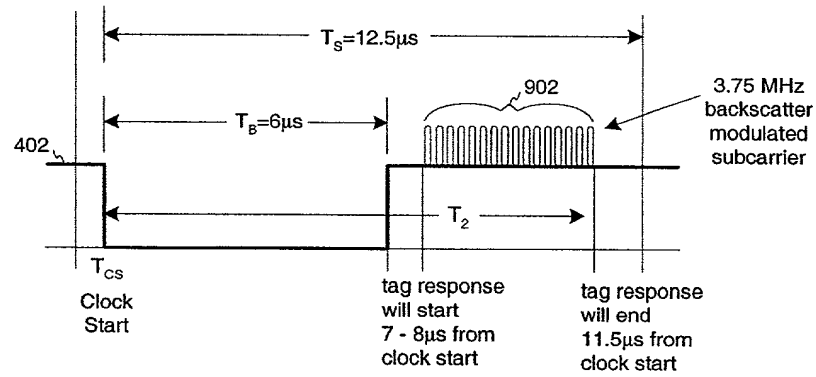


FIG. 9

FIG. 10

202120-000E/001

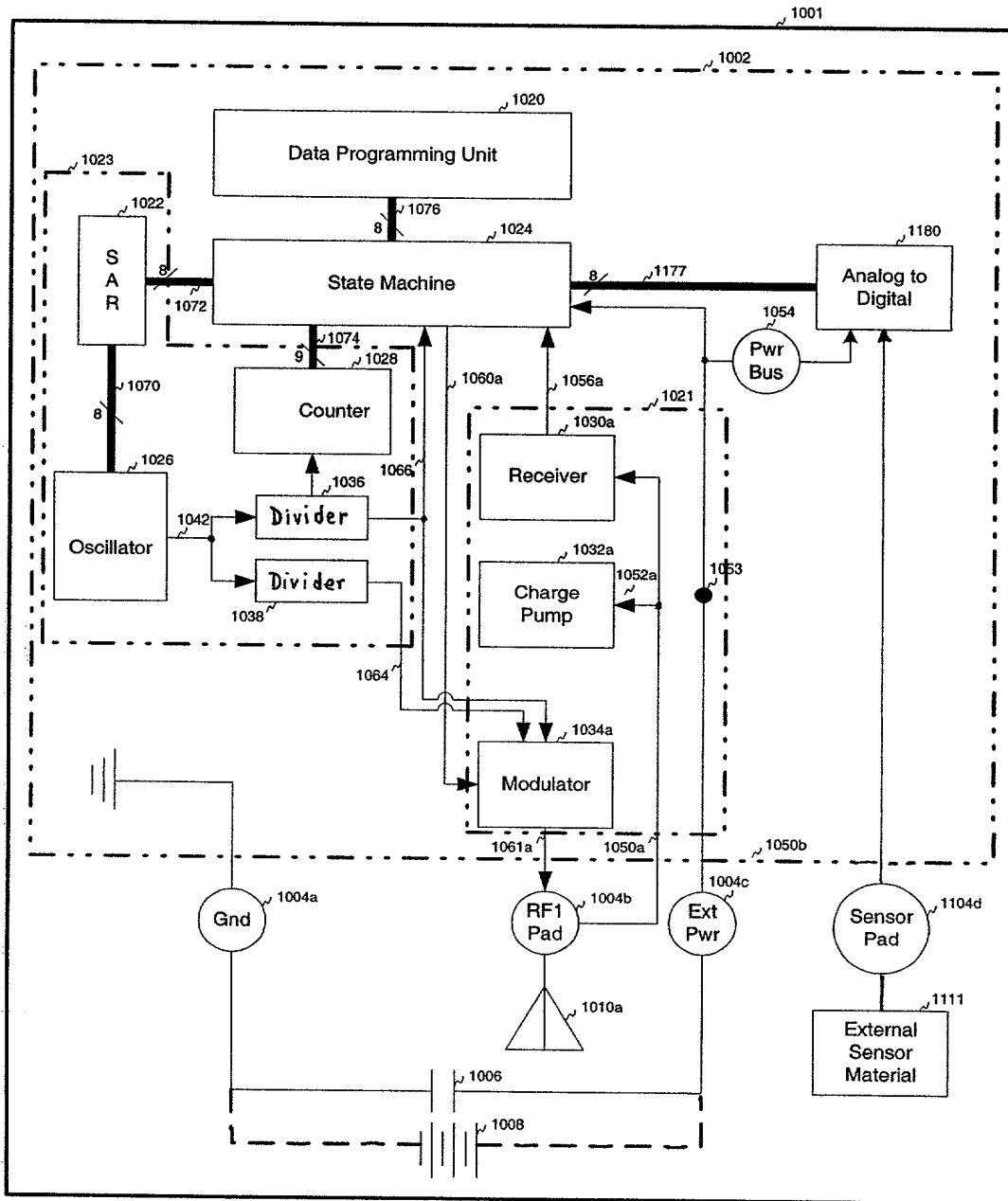


FIG. 11

2024-03-08 00:00:00

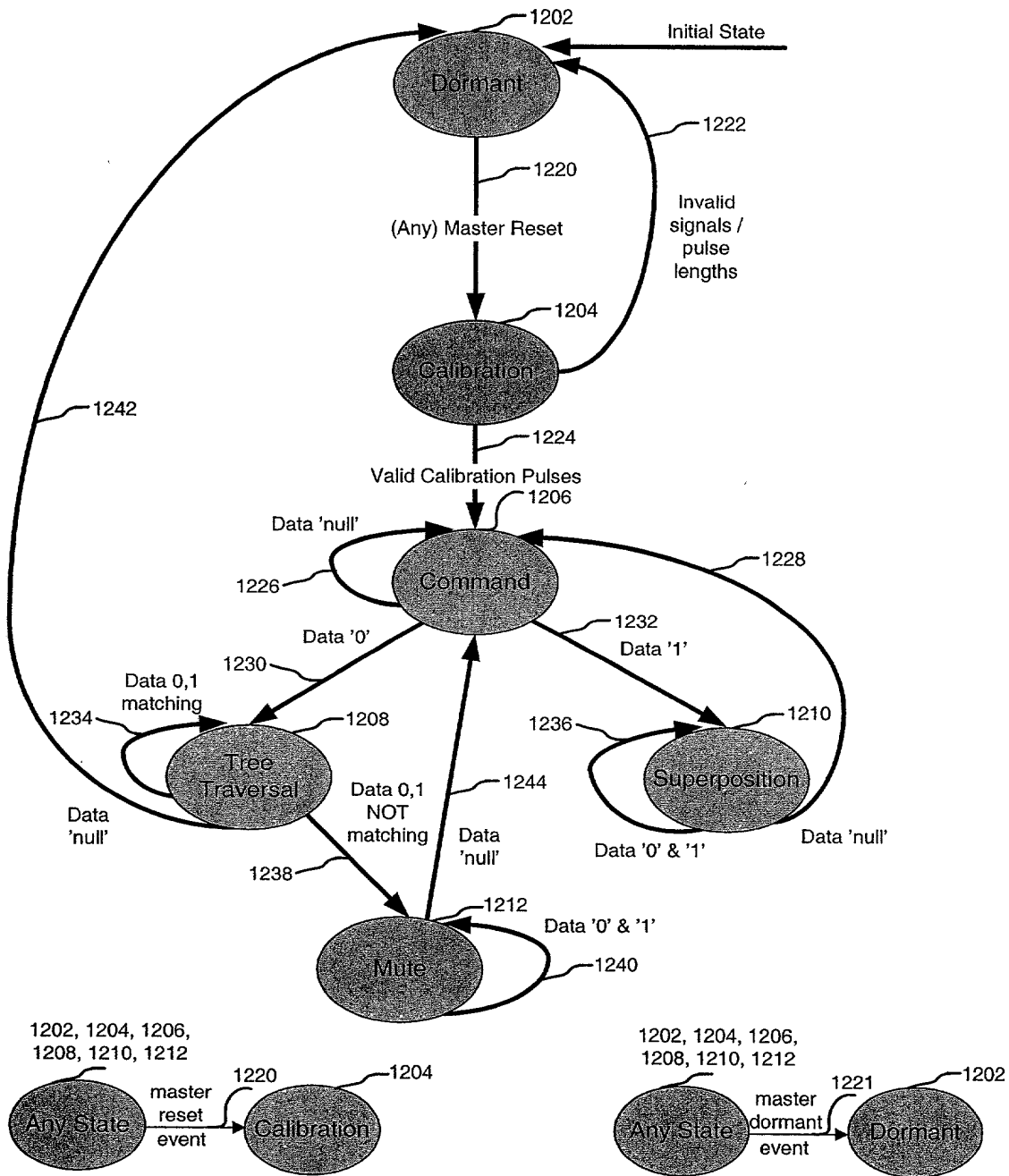


FIG. 12A

2025-03-20 00:02:00

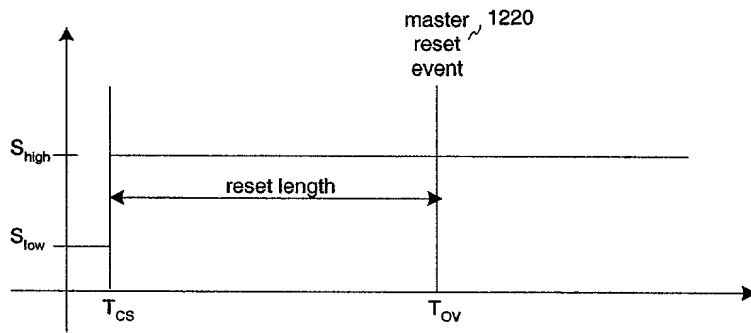


FIG. 12B

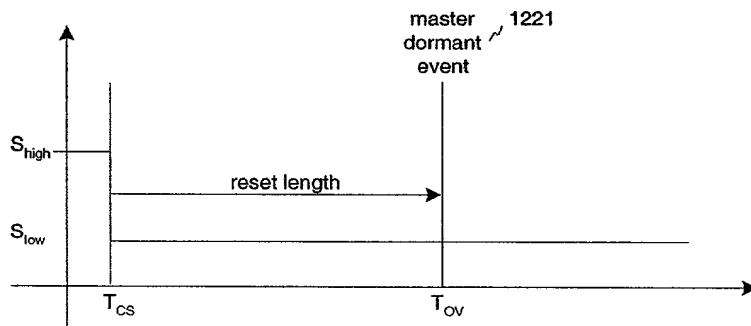


FIG. 12C

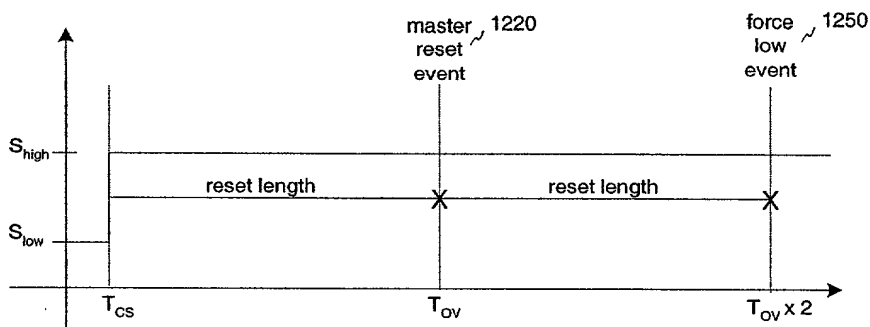


FIG. 12D

202120-000E200T

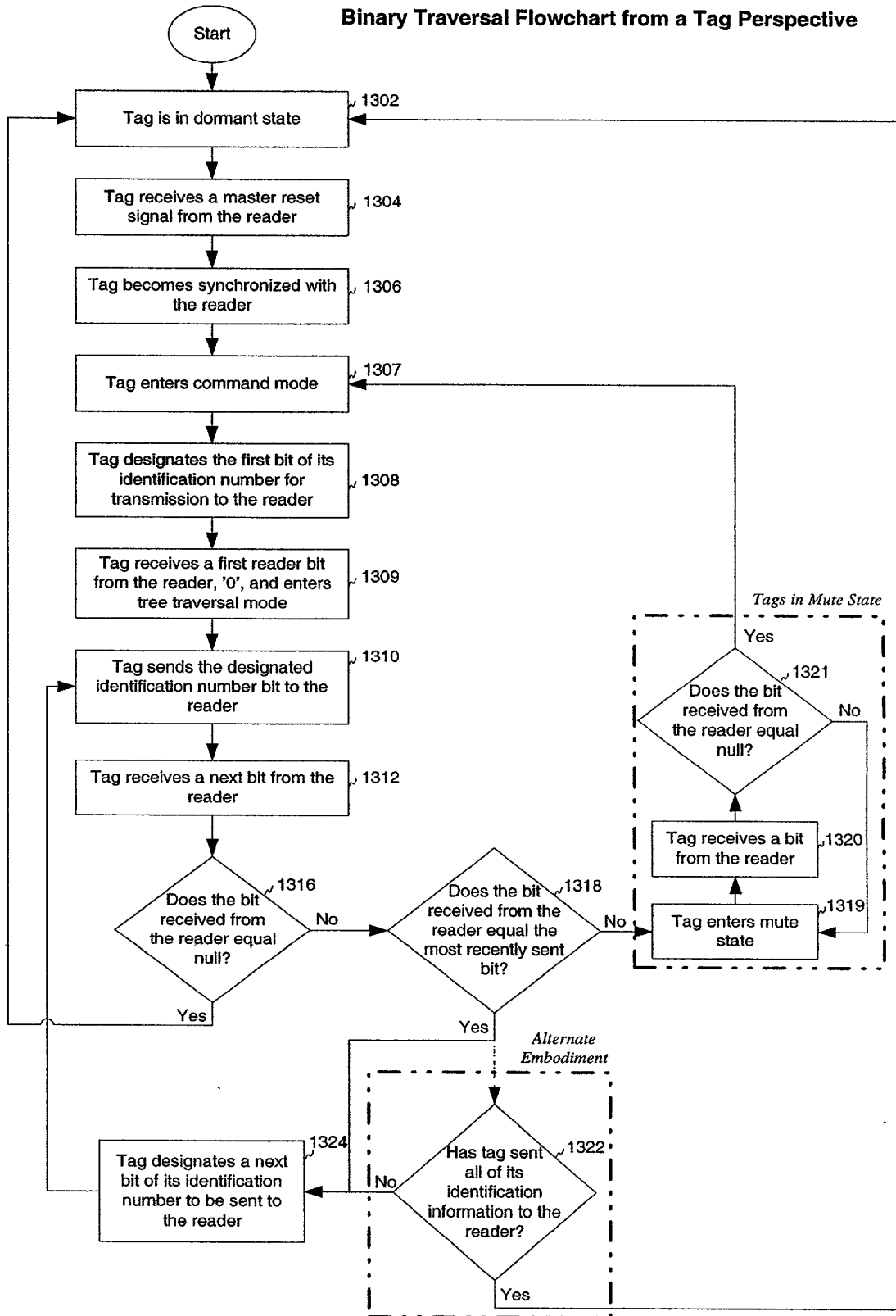


FIG.13

Read Specific Tag, Reader Perspective

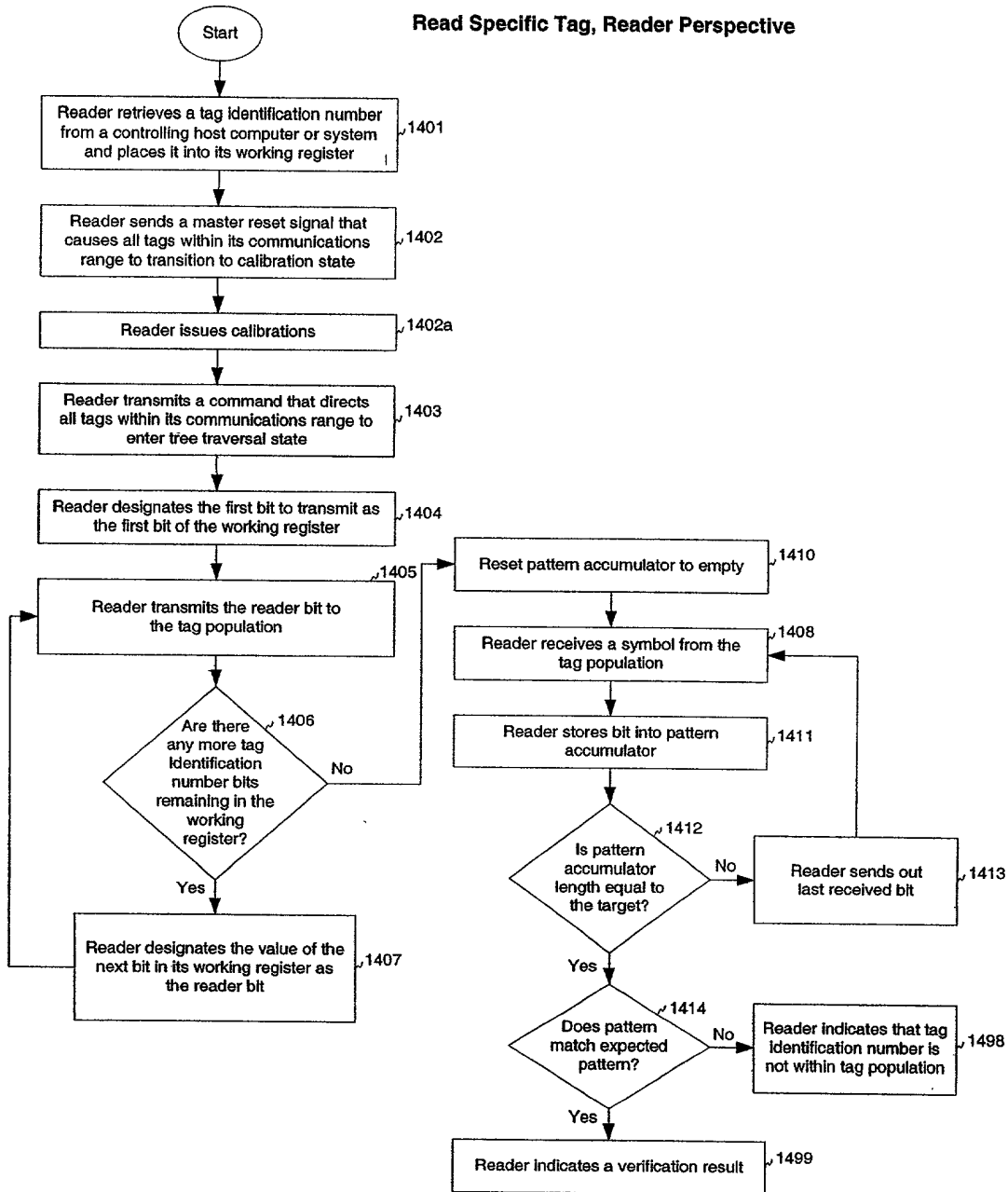


FIG. 14A

Read Specific Tag, Alt., Reader Perspective

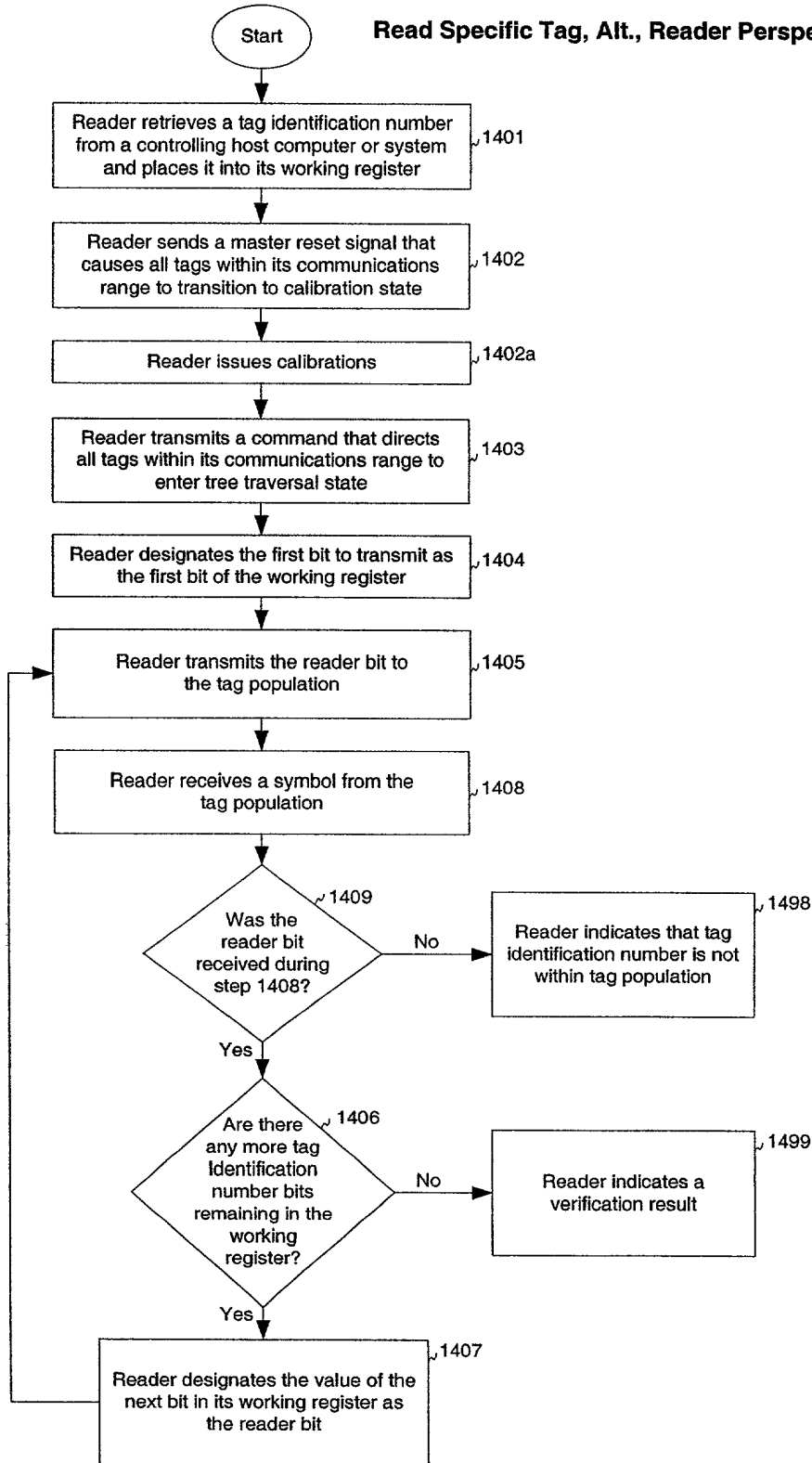


FIG. 14B

Read All Tags, Reader Perspective

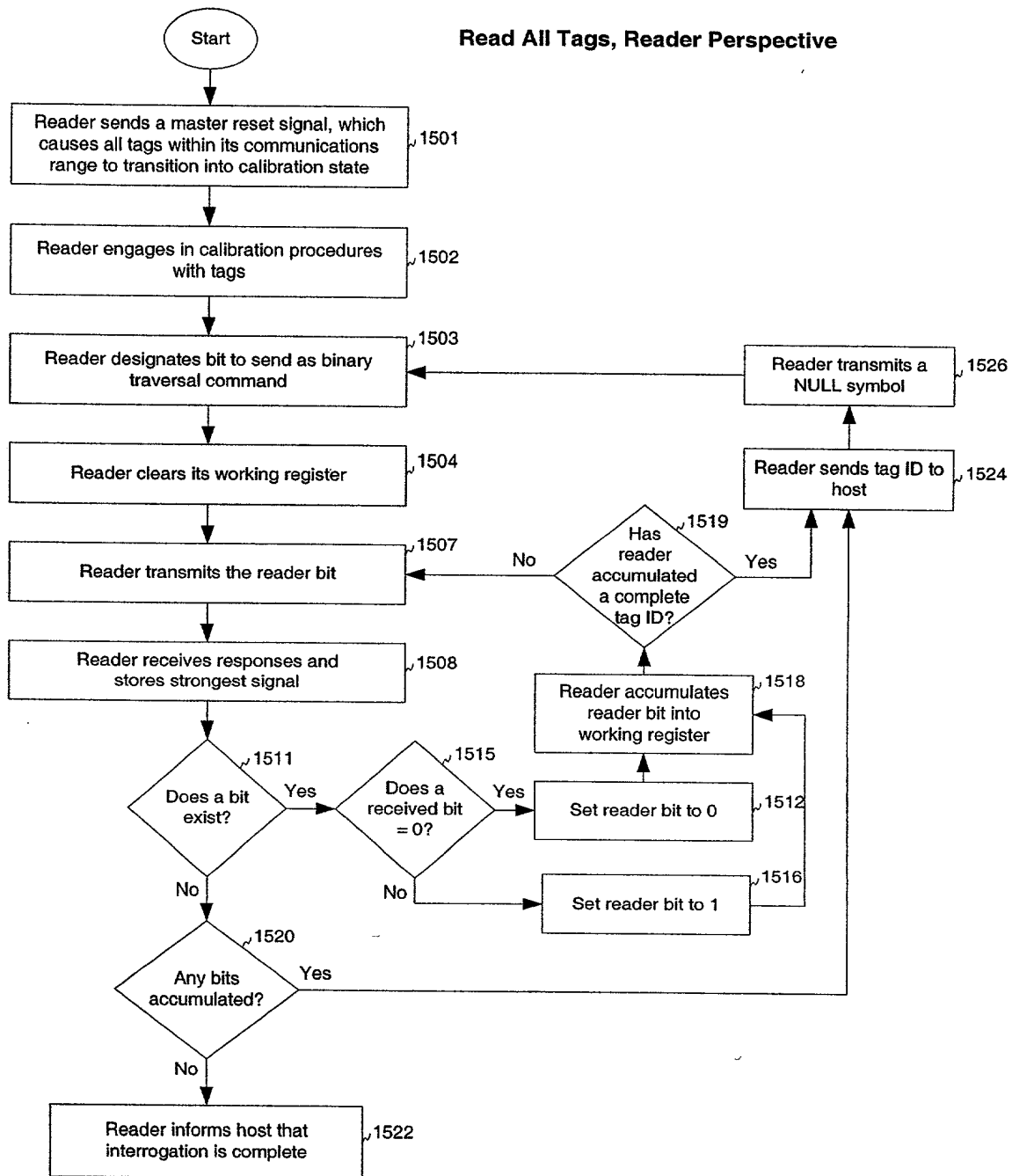


FIG. 15A :

Read All Tags, Alt., Reader Perspective

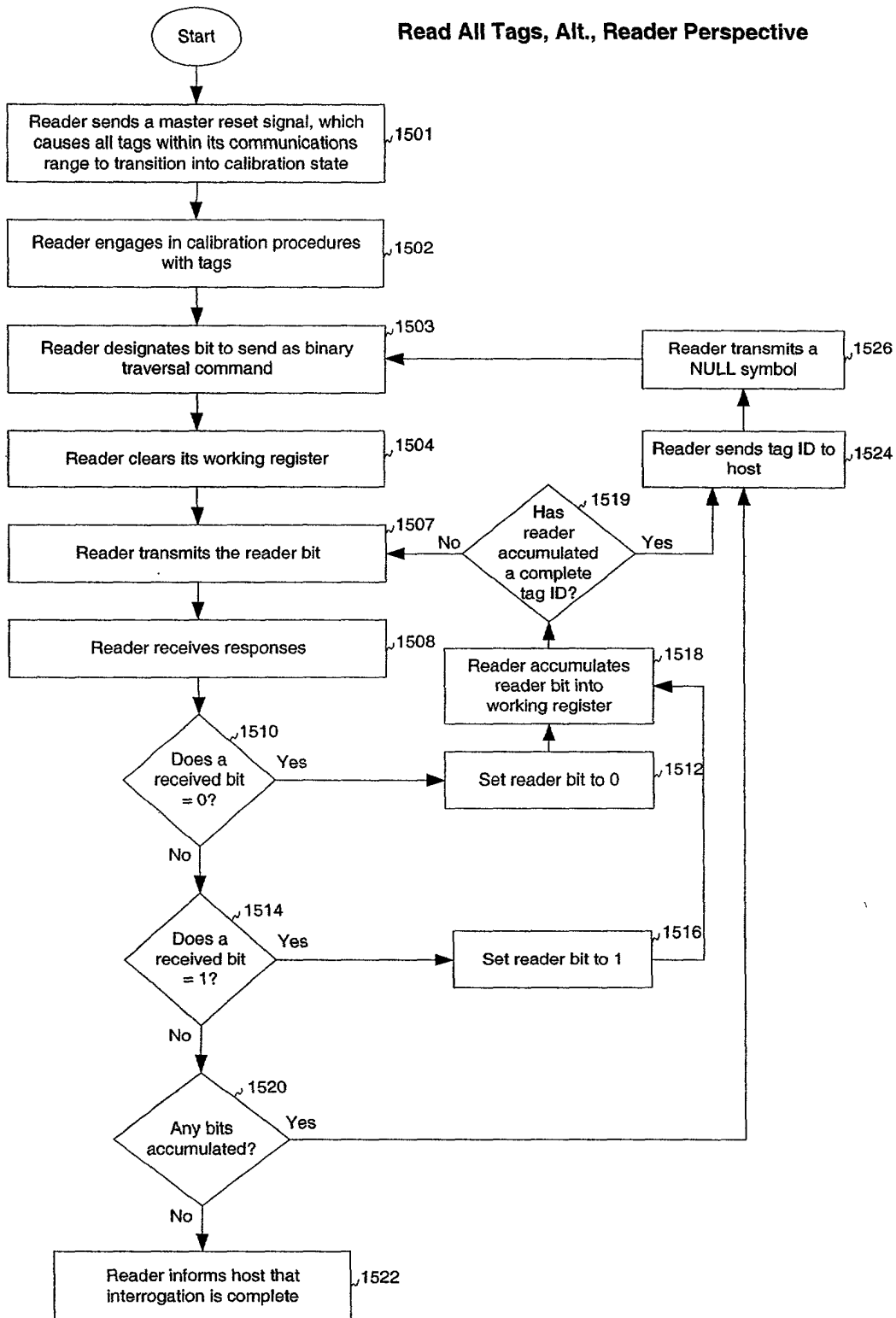


FIG. 15B

Binary Traversal Paths and Tree for a 3-bit Tag Population

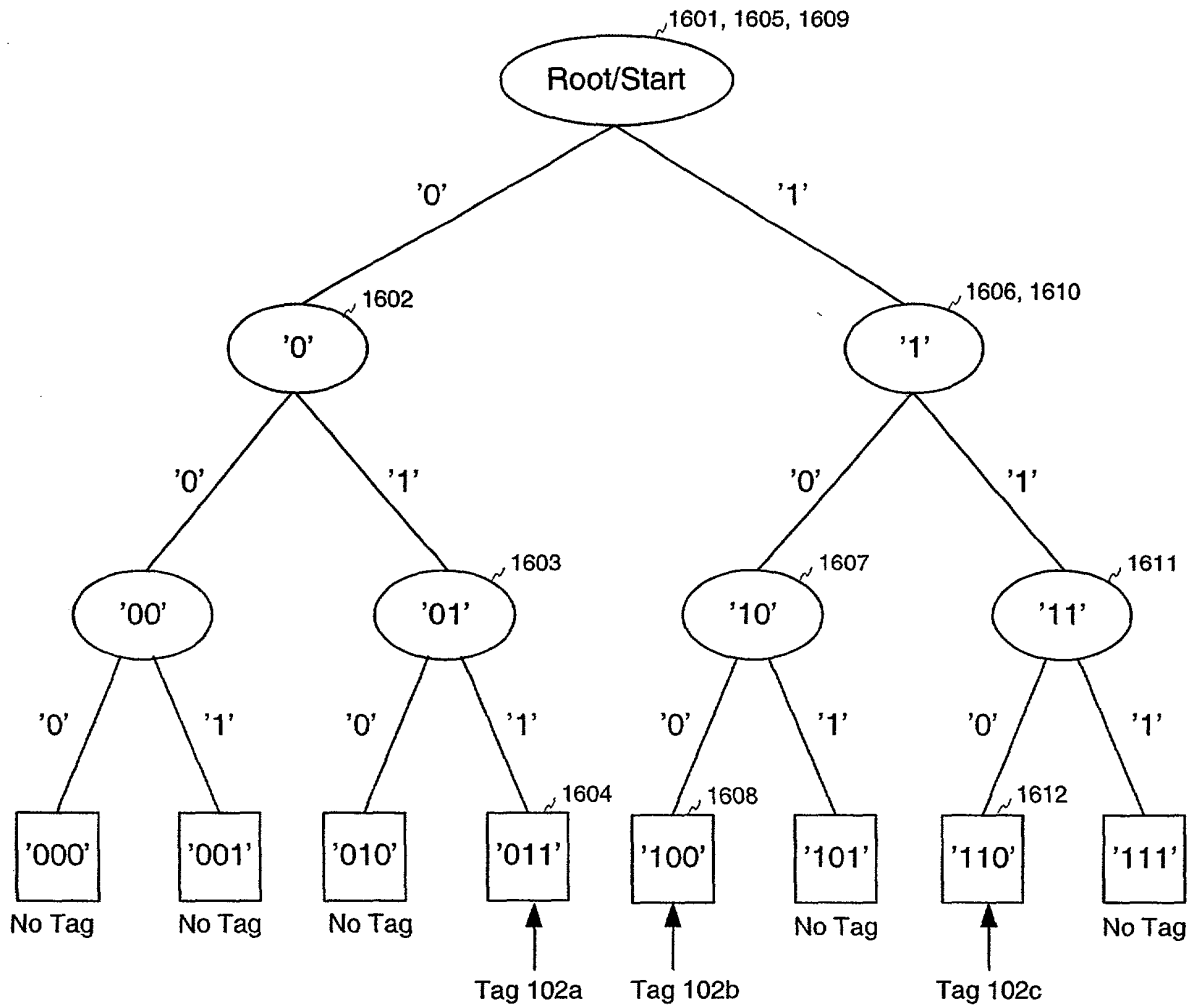


FIG. 16

Figure 1 shows the results of the 1000 iterations. The mean values of the parameters are shown in Table 1. The mean values of the parameters are shown in Table 1. The mean values of the parameters are shown in Table 1.

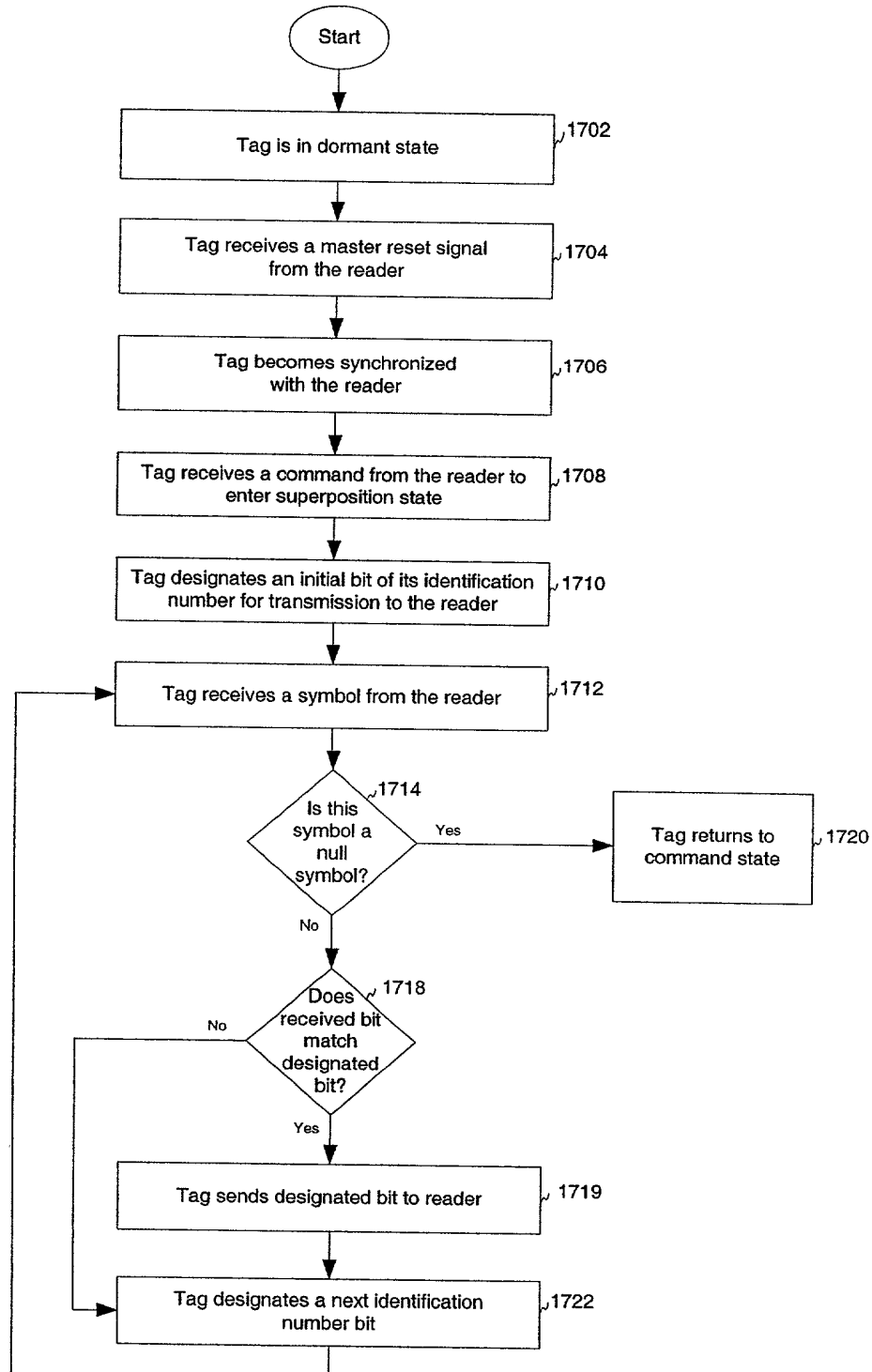


FIG. 17A

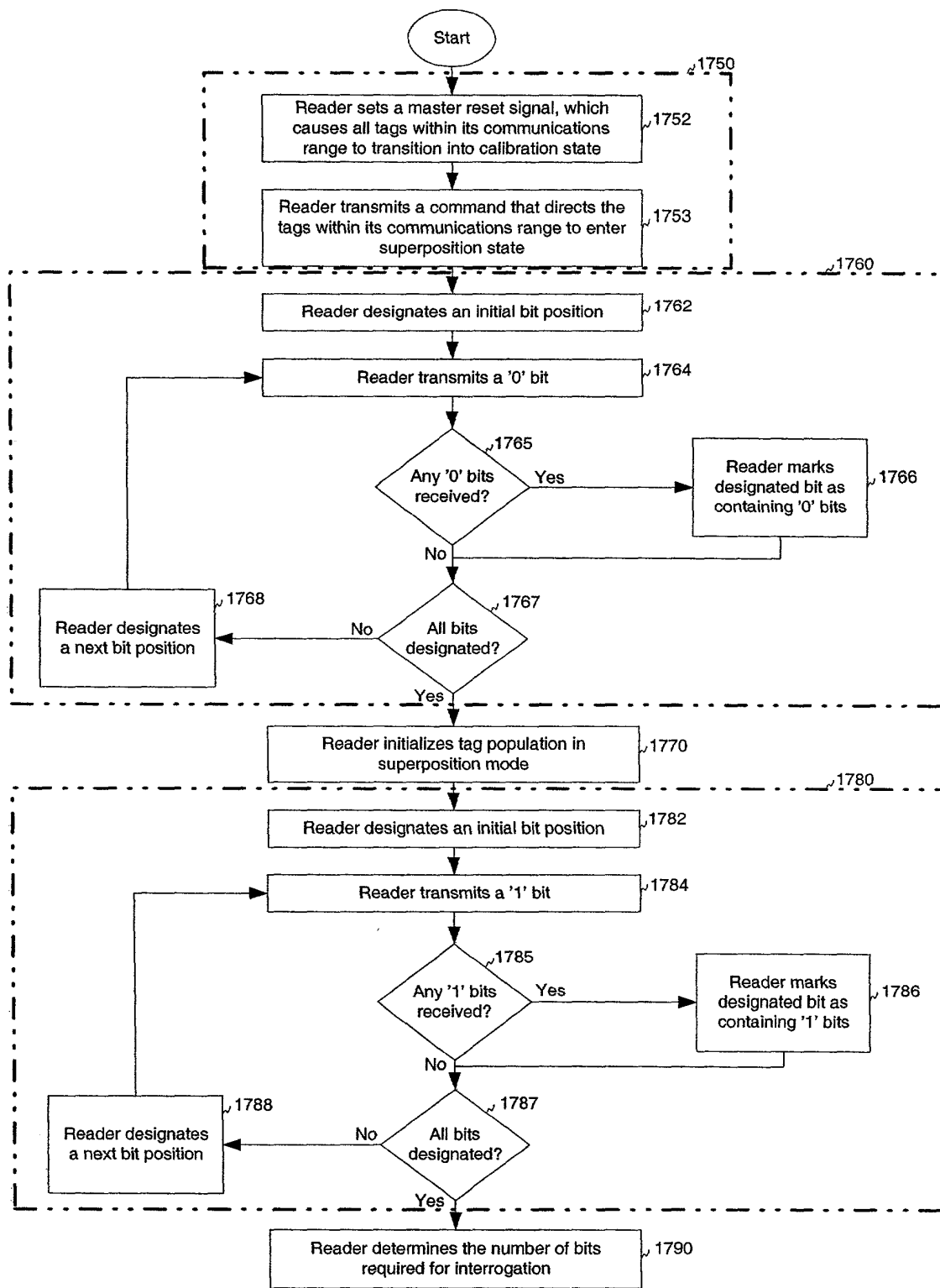


FIG. 17B

Figure 1. Schematic representation of the experimental design. The subjects were divided into two groups: the control group (CG) and the intervention group (IG). The CG received a standard diet and exercise program, while the IG received a low-carbohydrate, high-fat diet and a high-intensity interval training program. The study duration was 12 weeks. The primary outcome was the change in body composition, and the secondary outcome was the change in metabolic parameters.

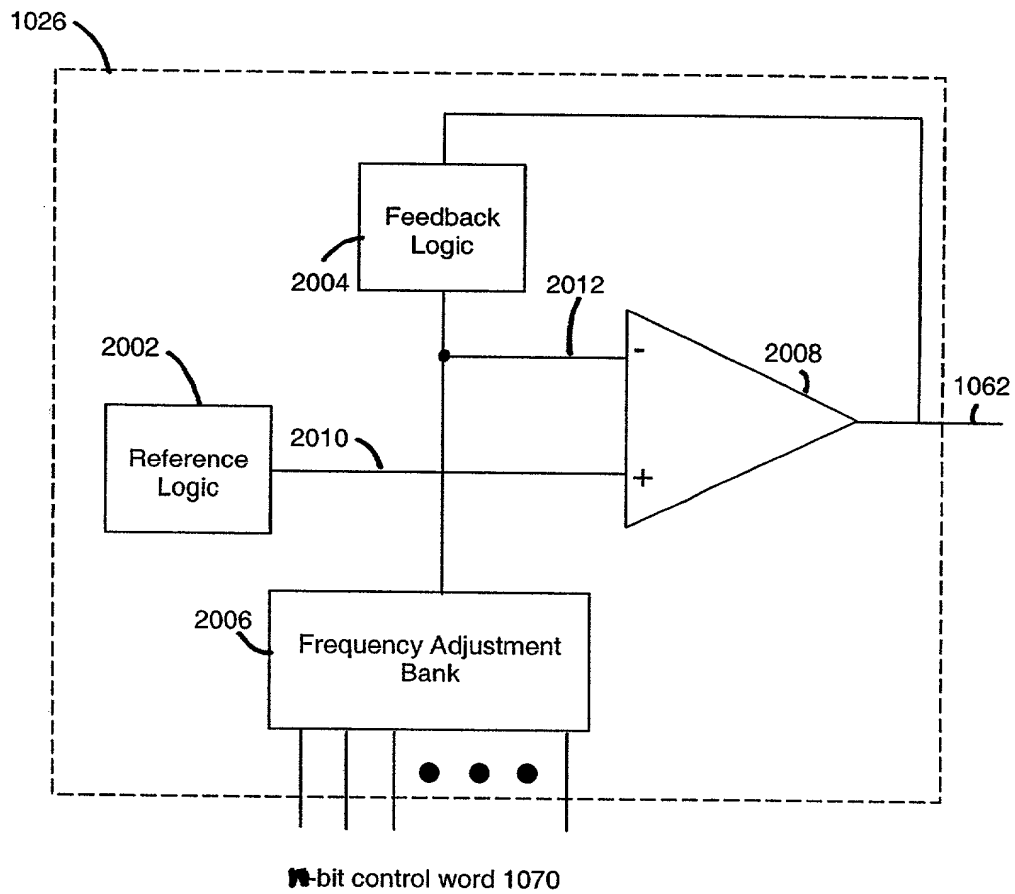


FIG. 20

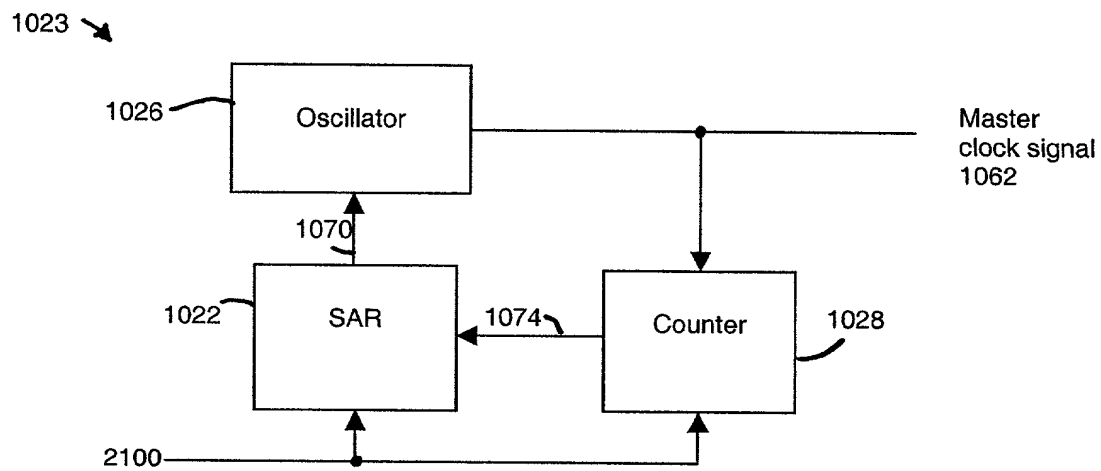


FIG. 21A

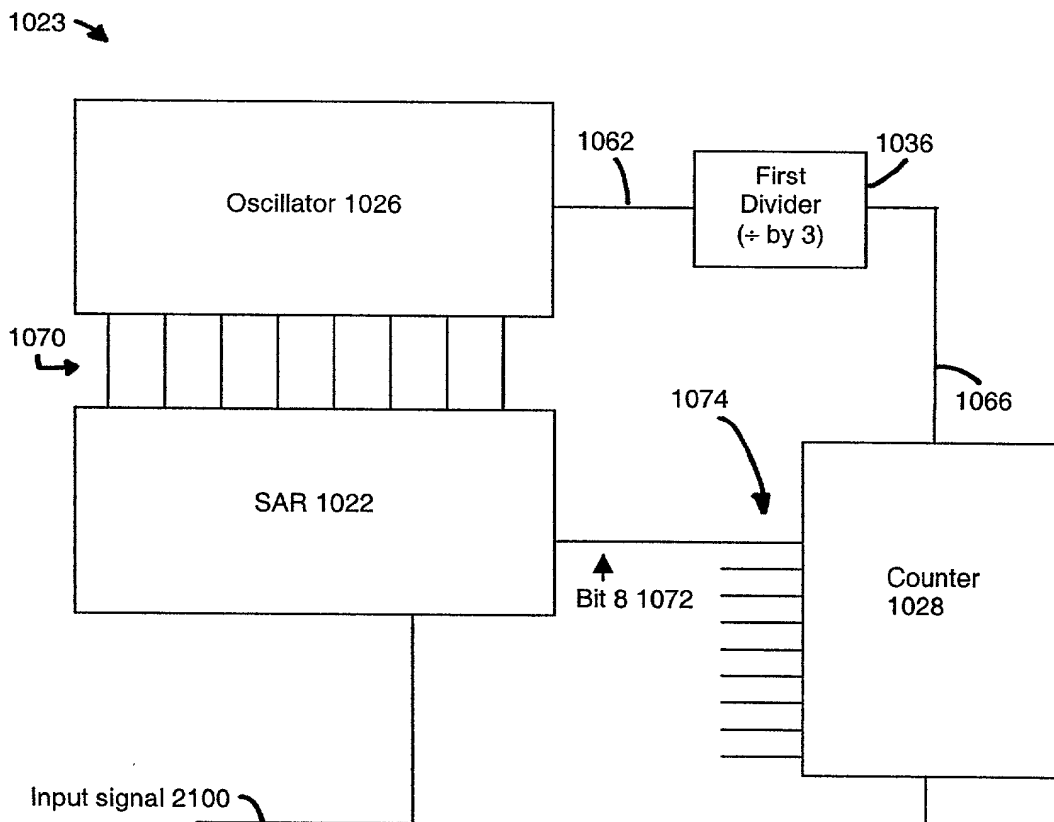


FIG. 21B

Conceptual implementation of SAR calibration

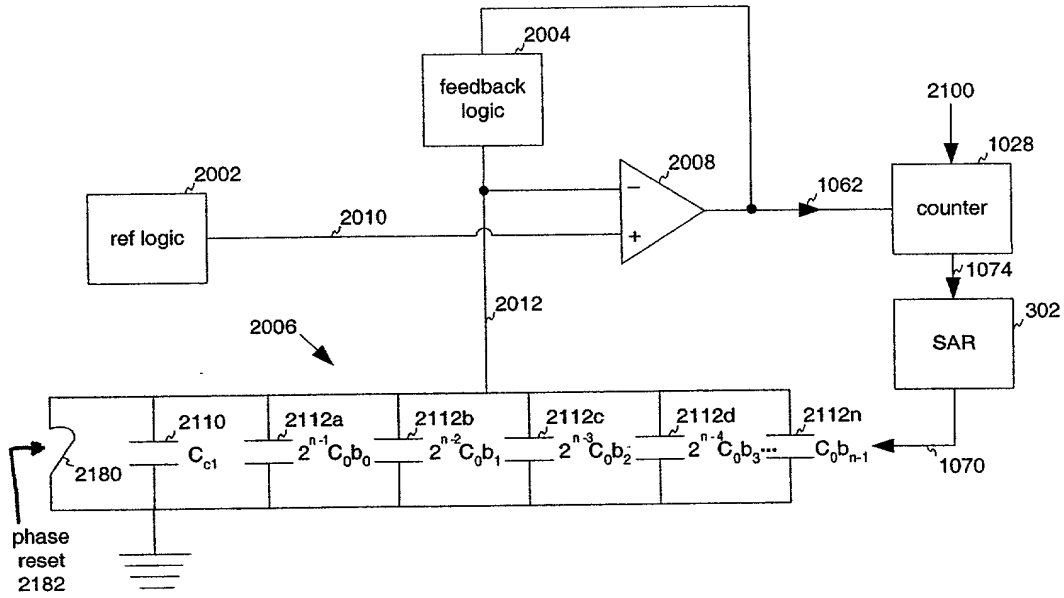


FIG. 21C

Physical implementation of adjustable capacitor bank

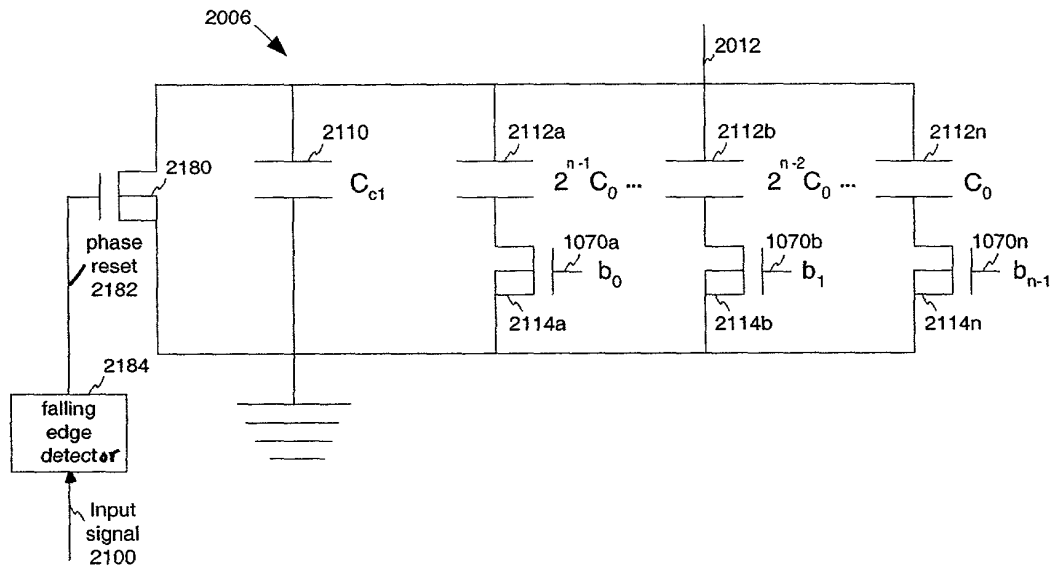
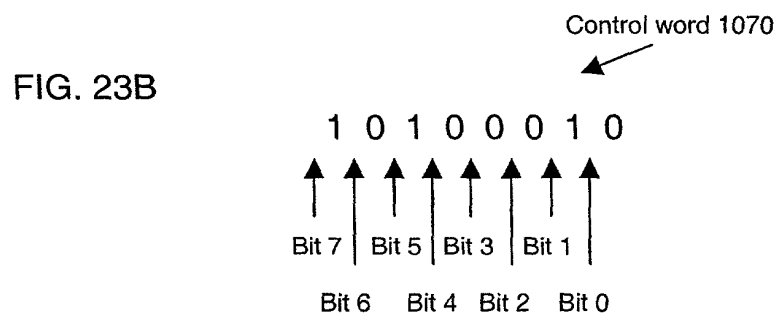
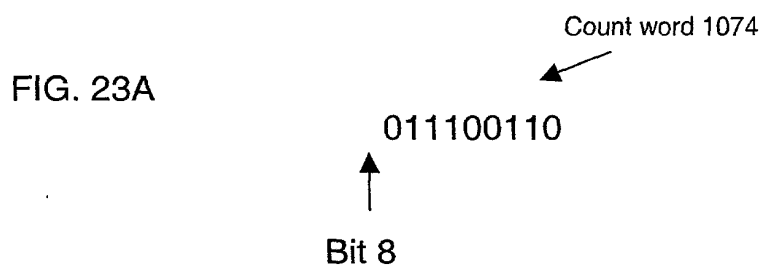
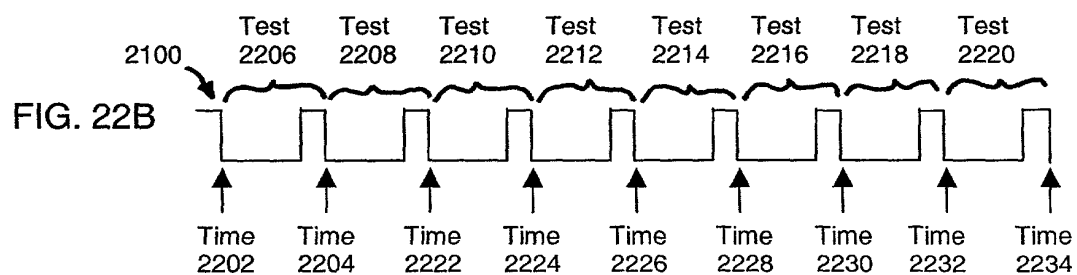
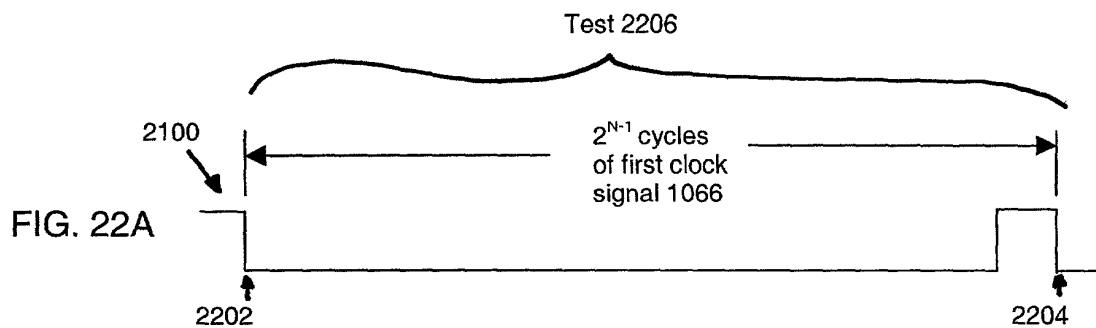


FIG. 21D



1022

N-bit control word 1070

2402

N Bit Register Bank

2406

Logic/State Machine

2404

Bit 8 of Count Word 1074

Input signal 2100

FIG. 24

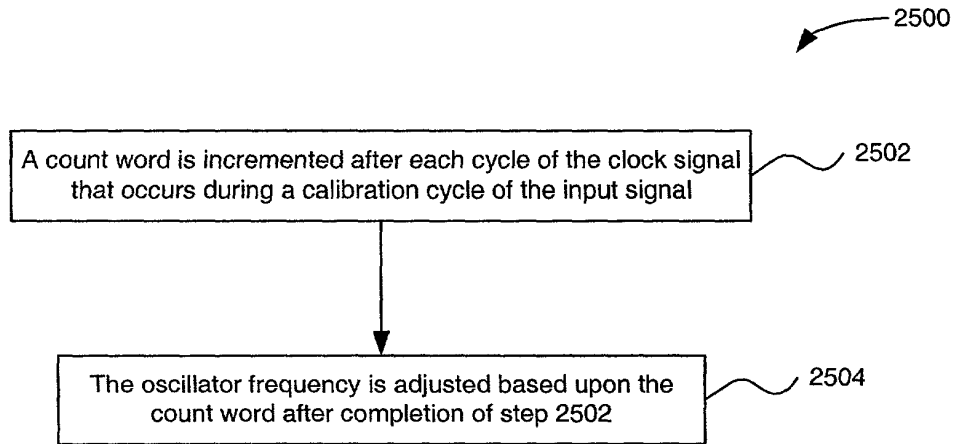


FIG. 25A

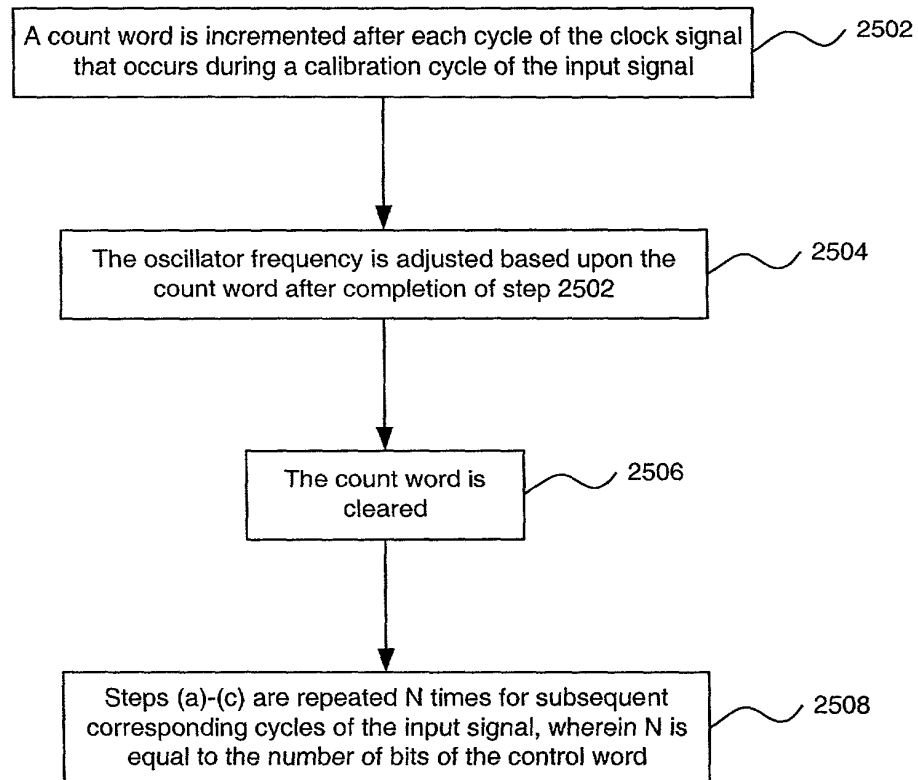


FIG. 25B

2021-20 00000000

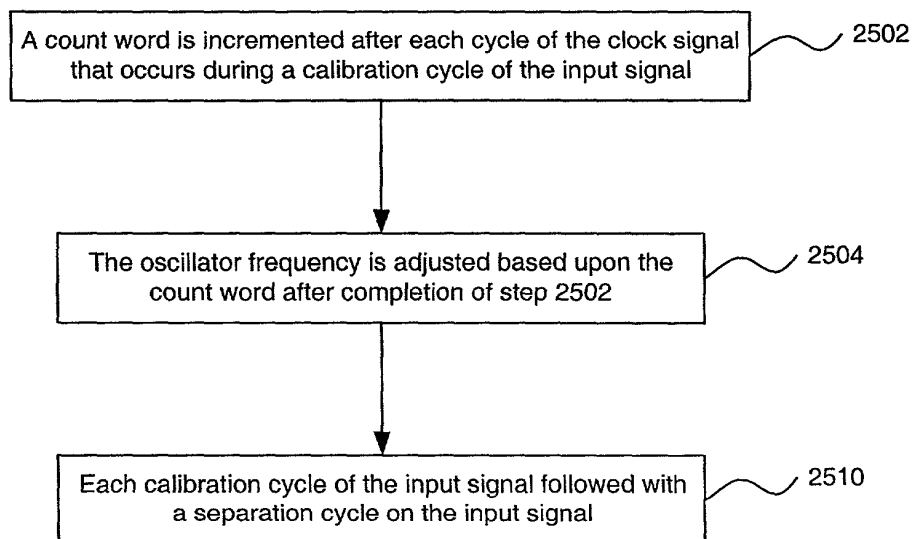


FIG. 25C

2025T20"000E2001

Data Symbol Timing Chart

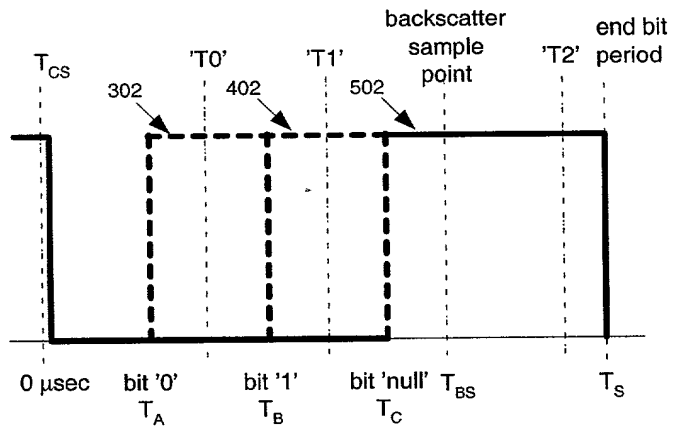
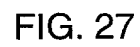


FIG. 26B



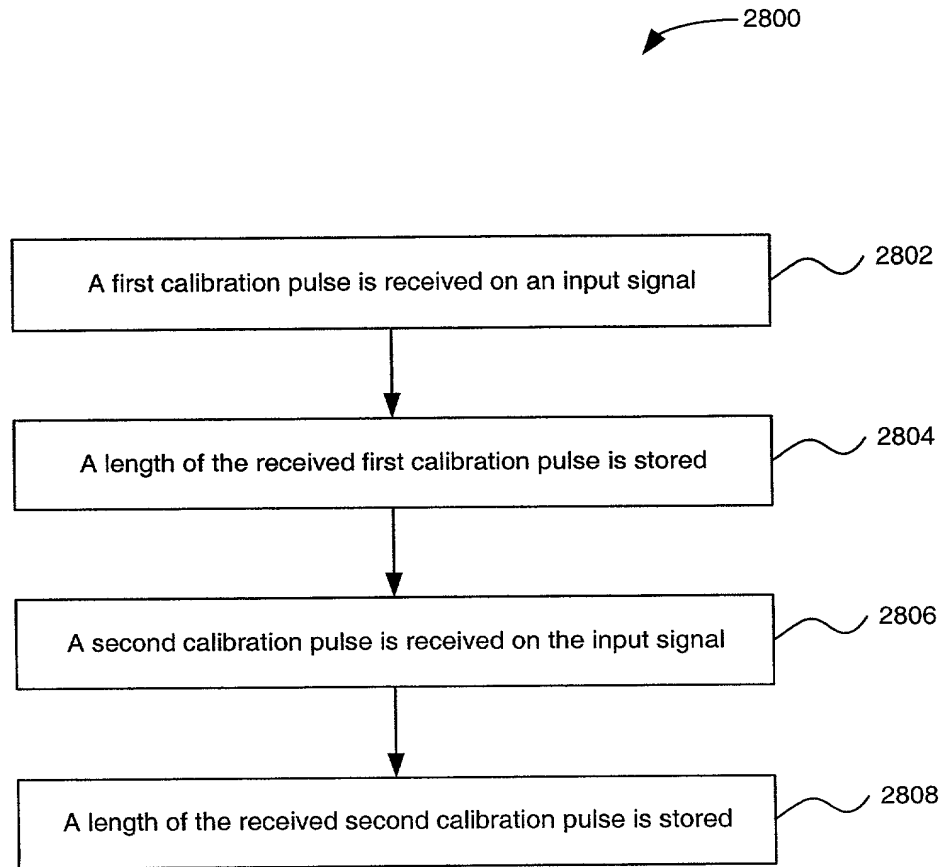
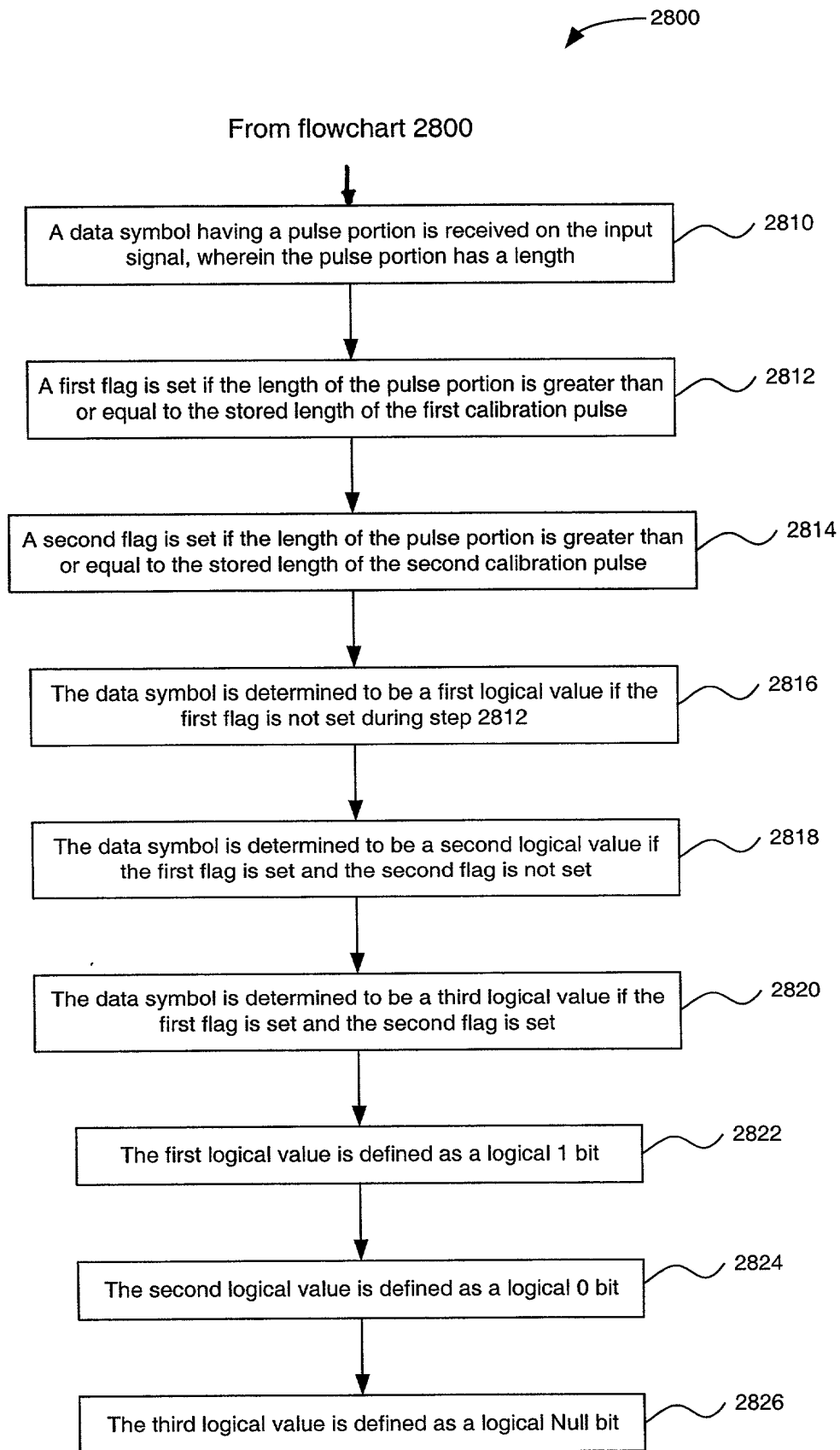


FIG. 28A

**FIG. 28B**

2025-03-03 00:00:00

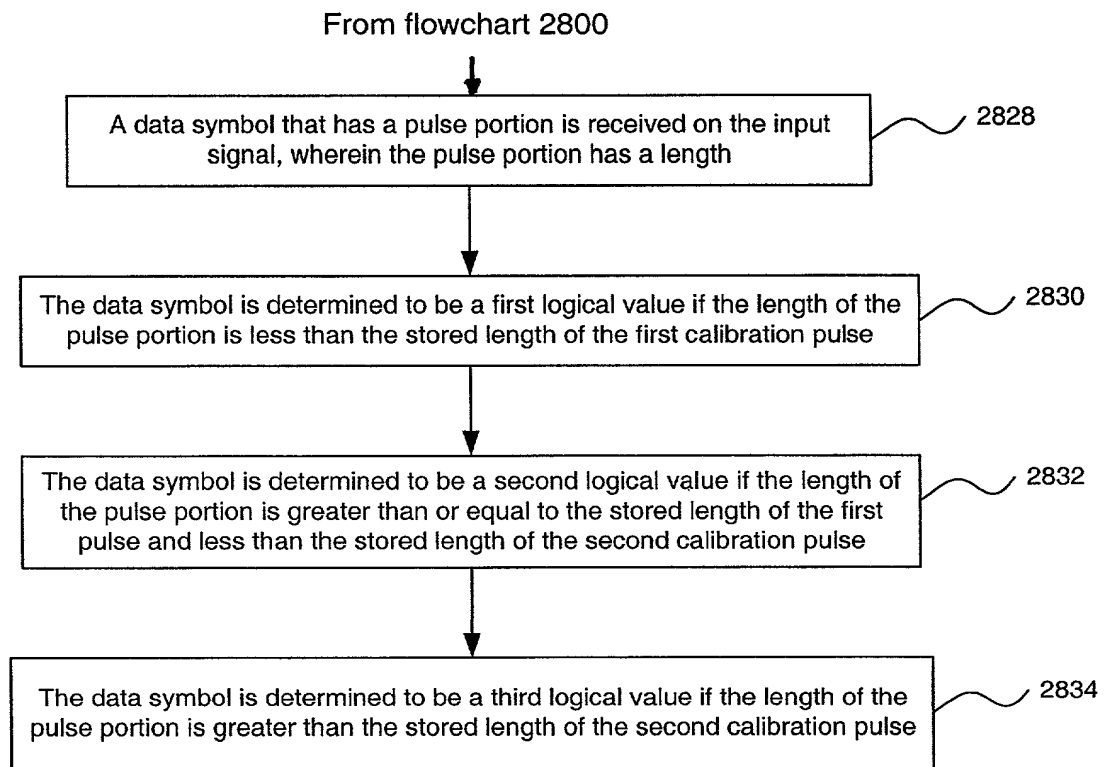


FIG. 28C

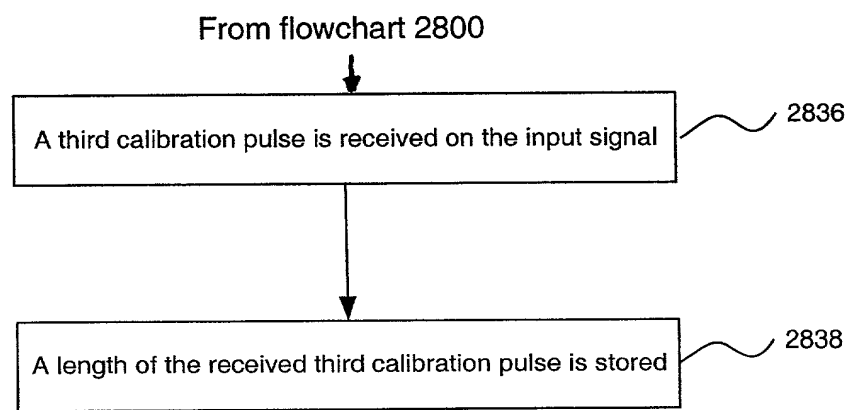


FIG. 28D

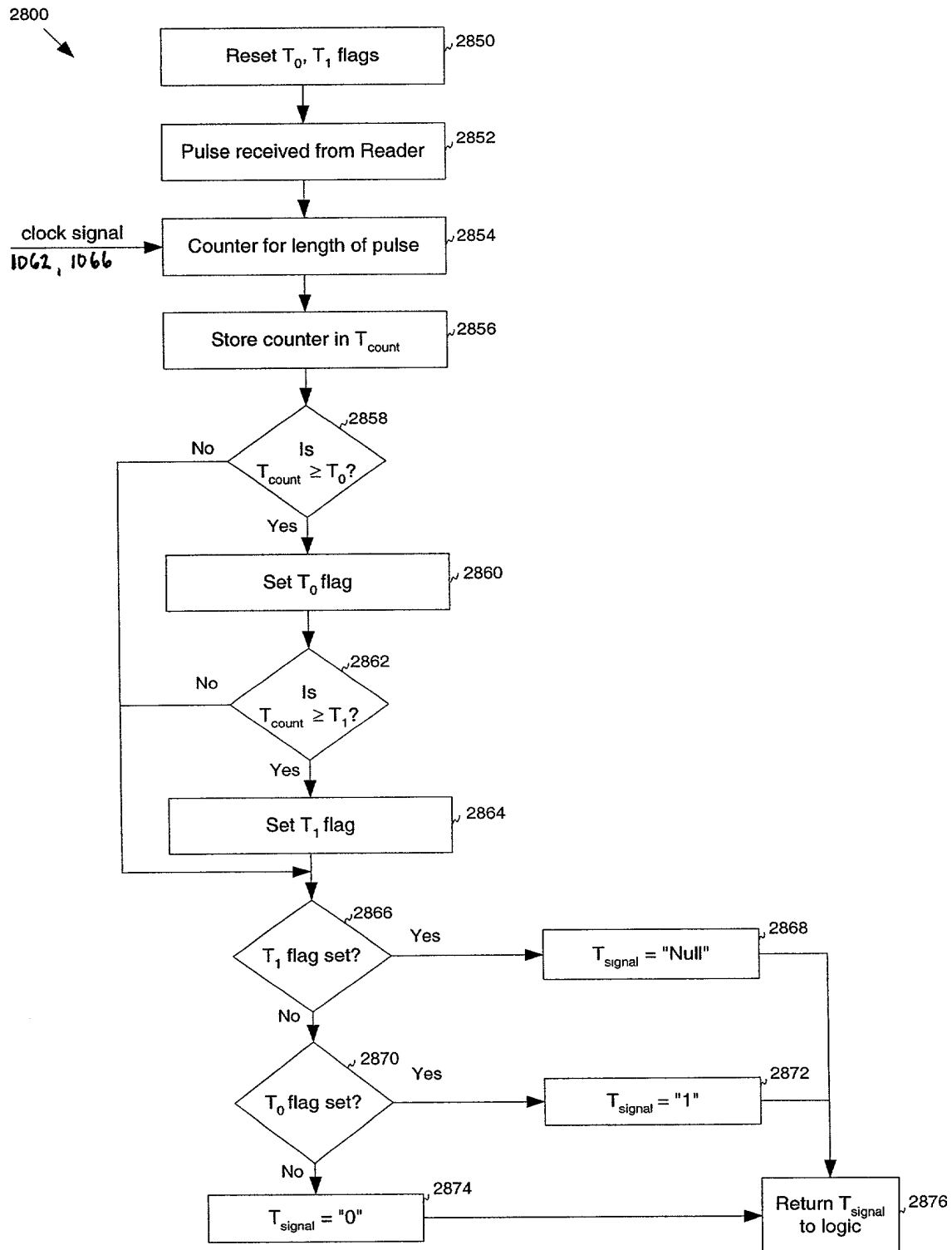


FIG. 28E

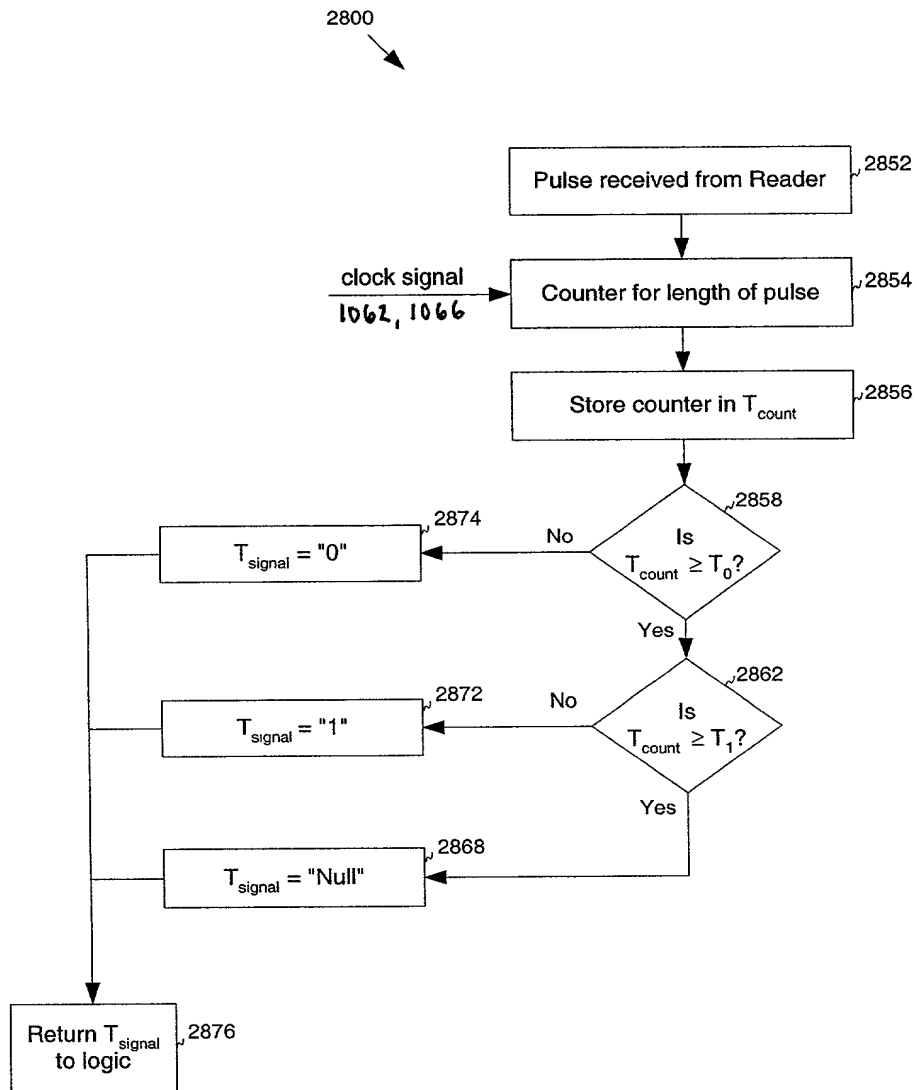
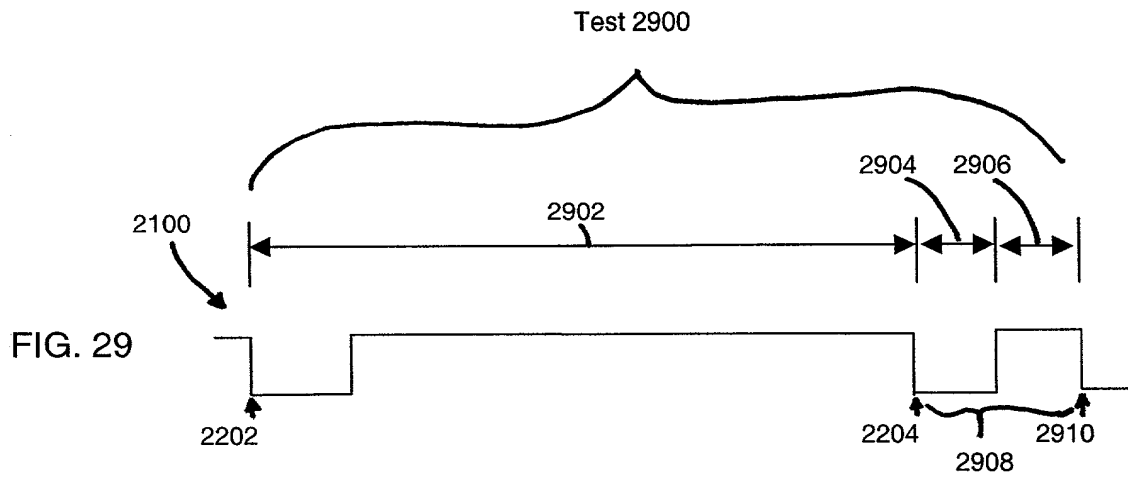
[illegible]

FIG. 28F



Analog Front End

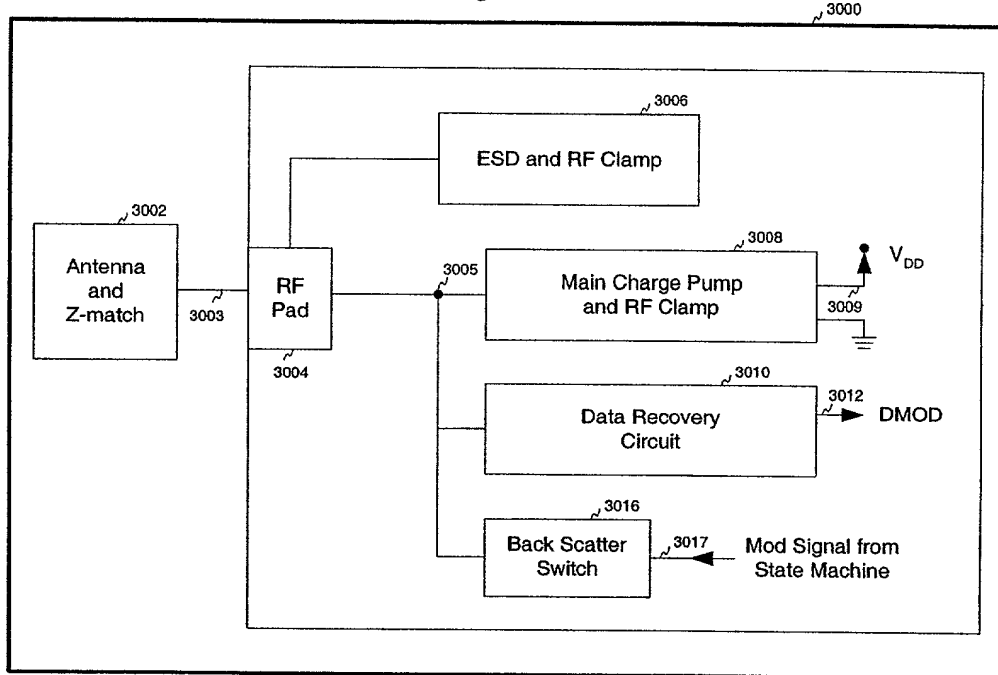


FIG. 30

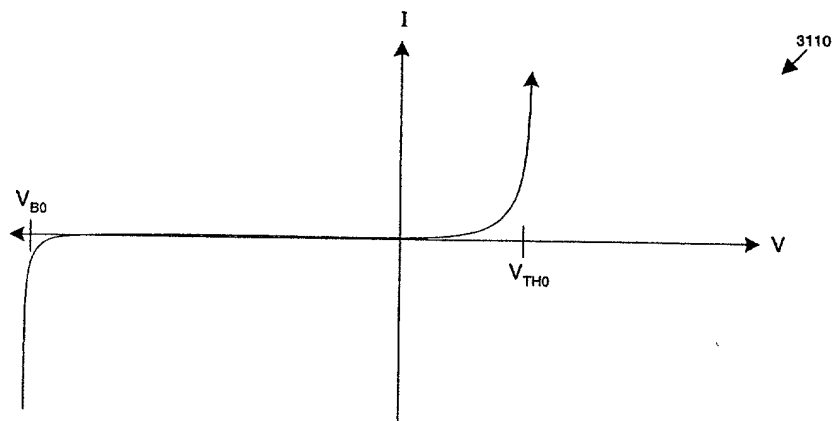


FIG. 32A

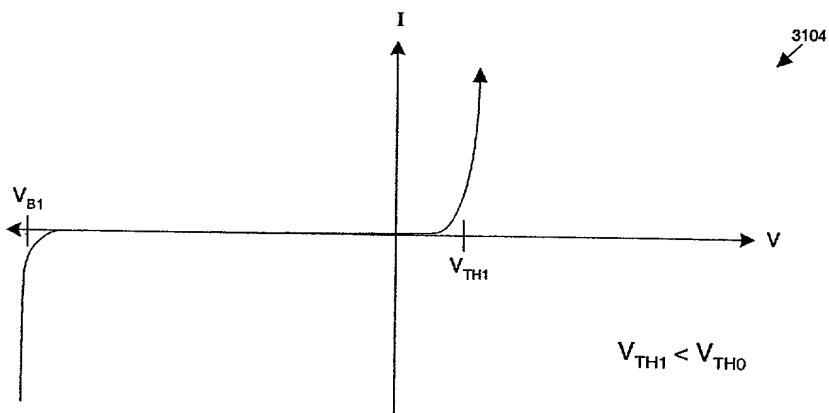


FIG. 32B

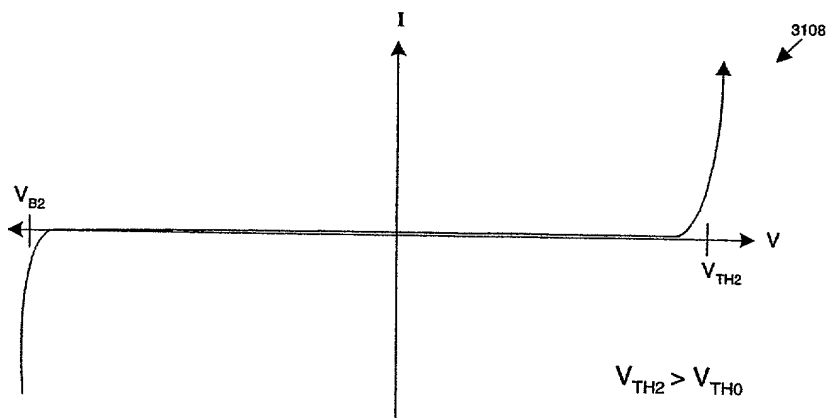


FIG. 32C

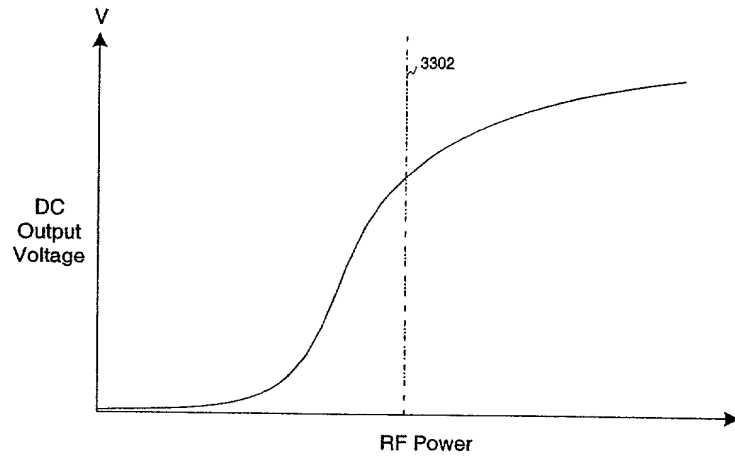


FIG. 33A

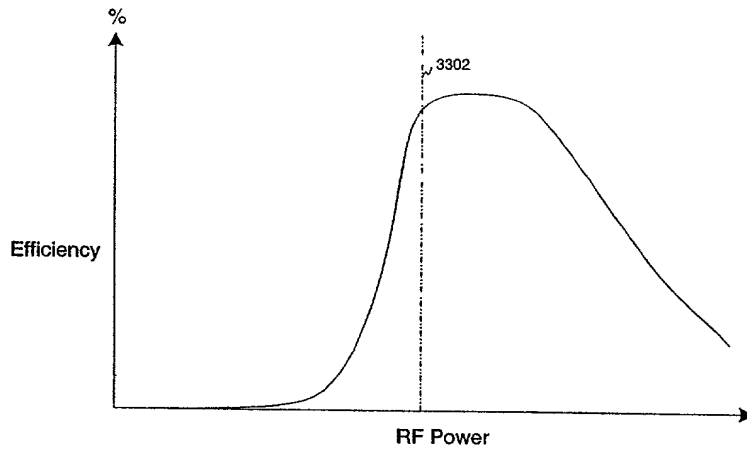


FIG. 33B

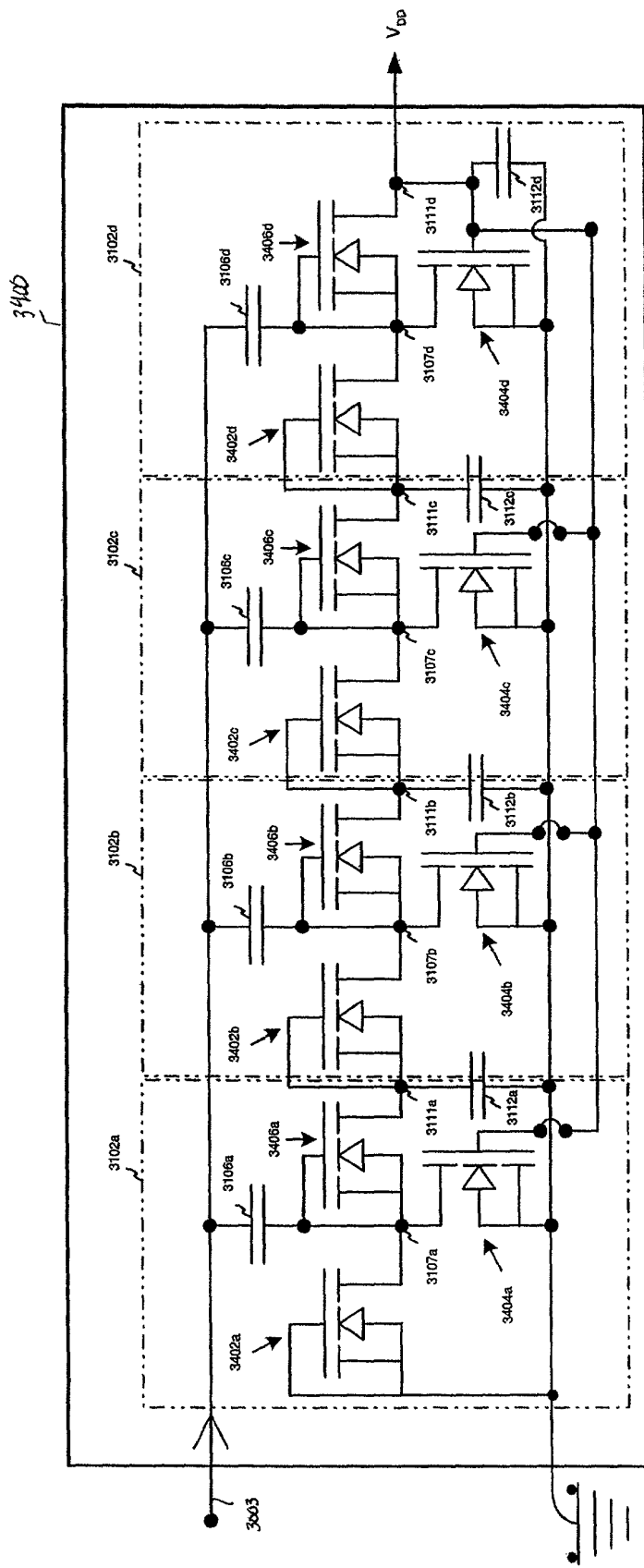


FIG. 34A

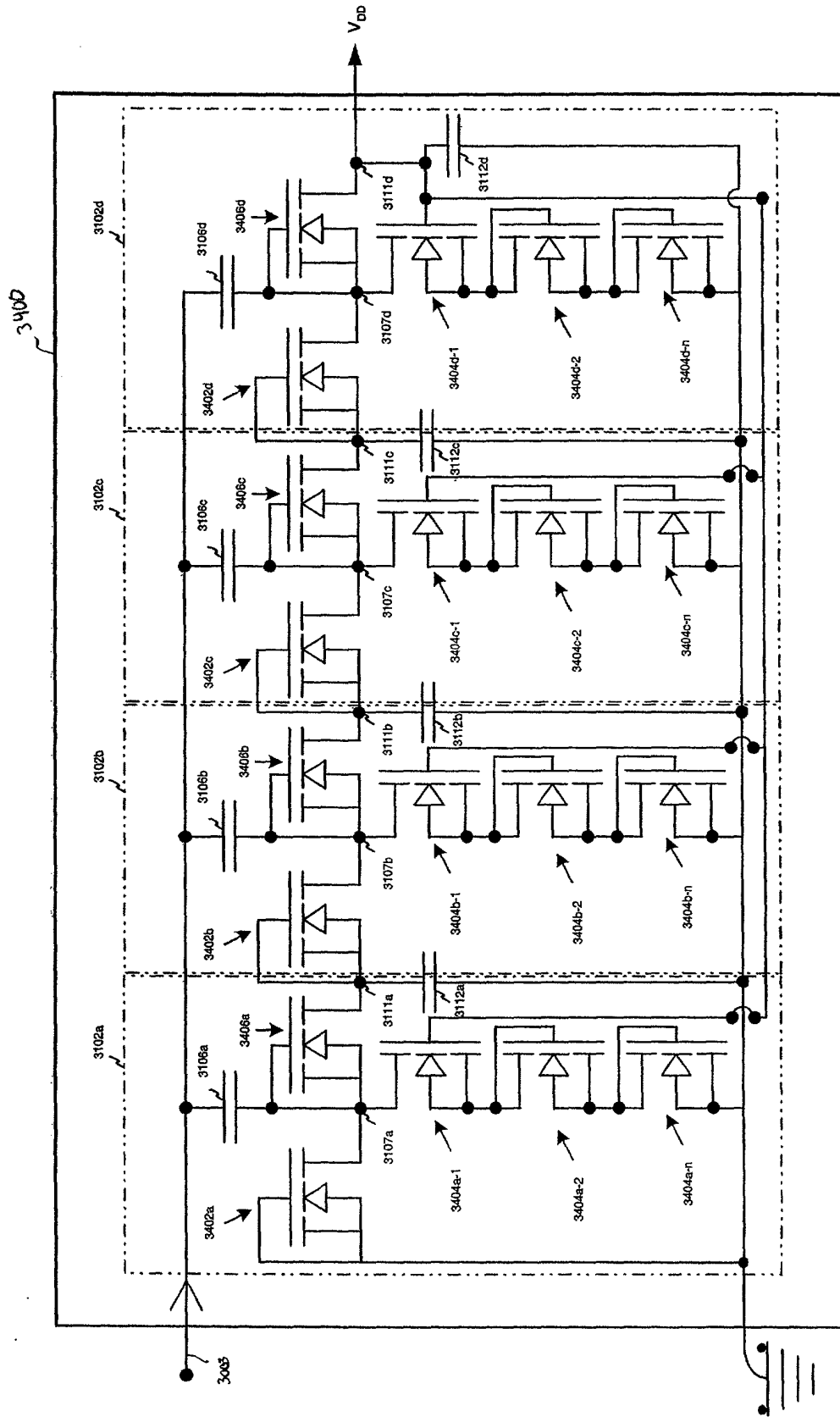


FIG. 34B

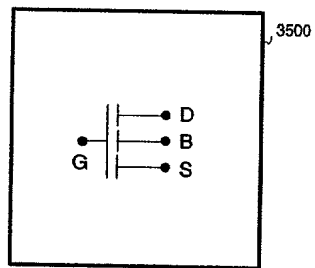


FIG. 35A

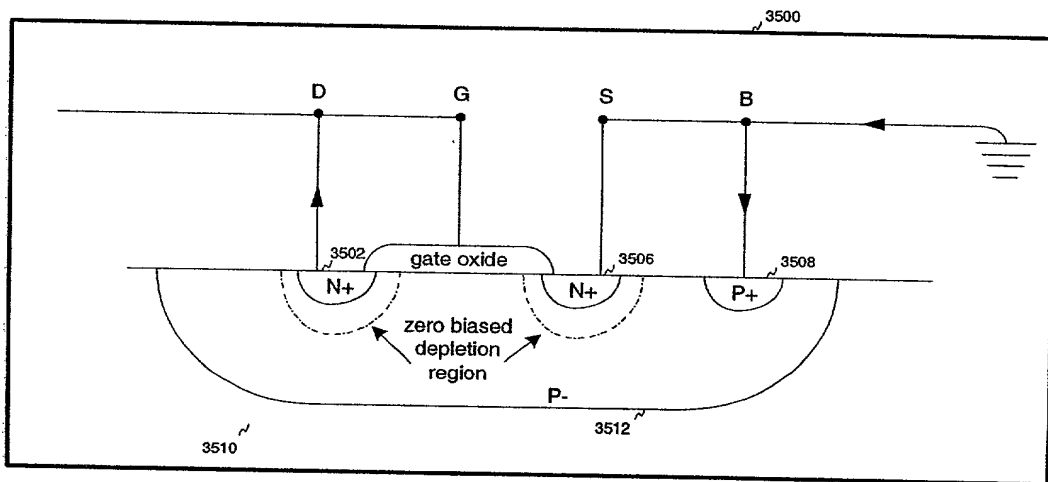


FIG. 35B

20250820-00027001

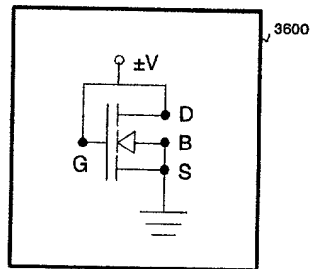


FIG. 36A

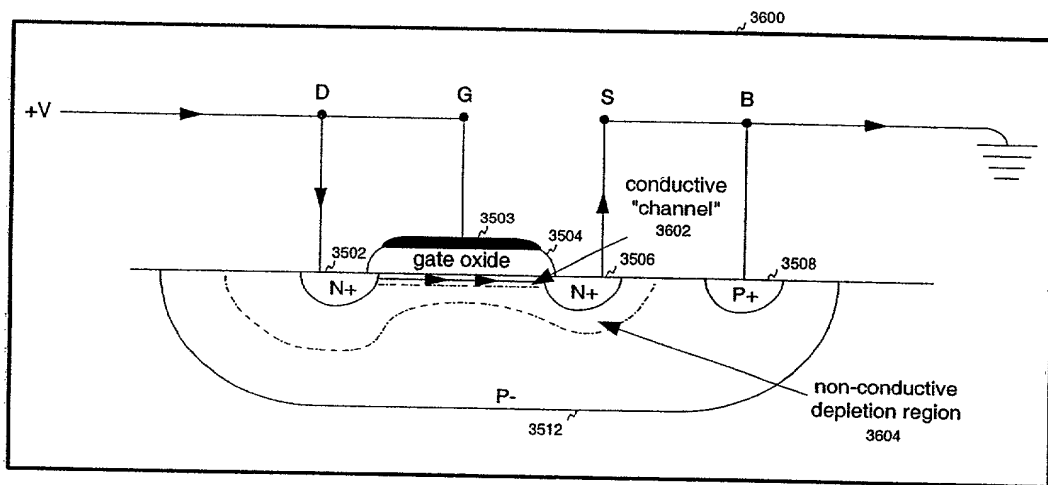


FIG. 36B

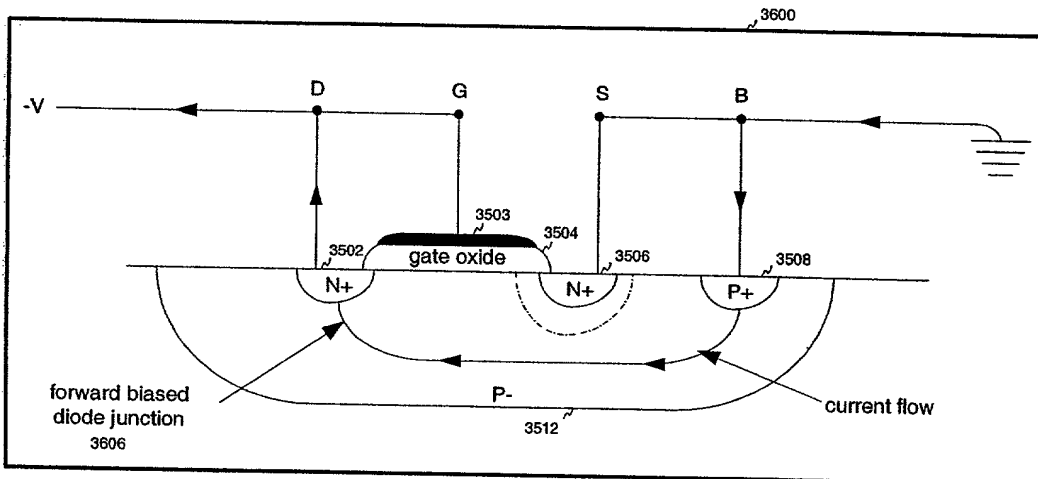


FIG. 36C

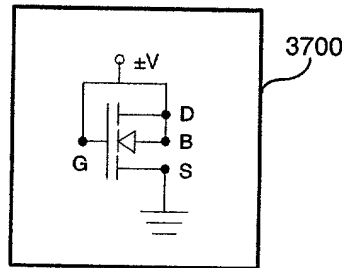


FIG. 37A

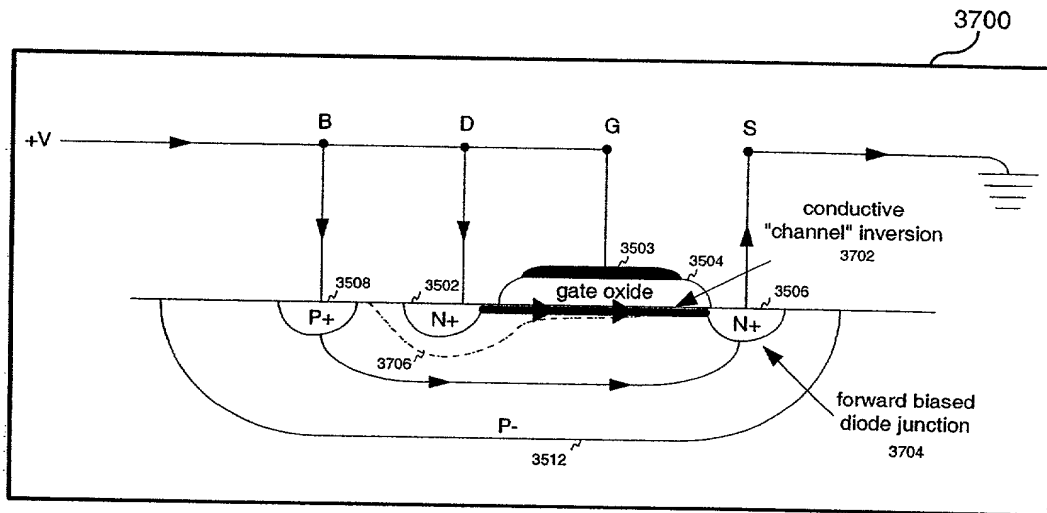


FIG. 37B

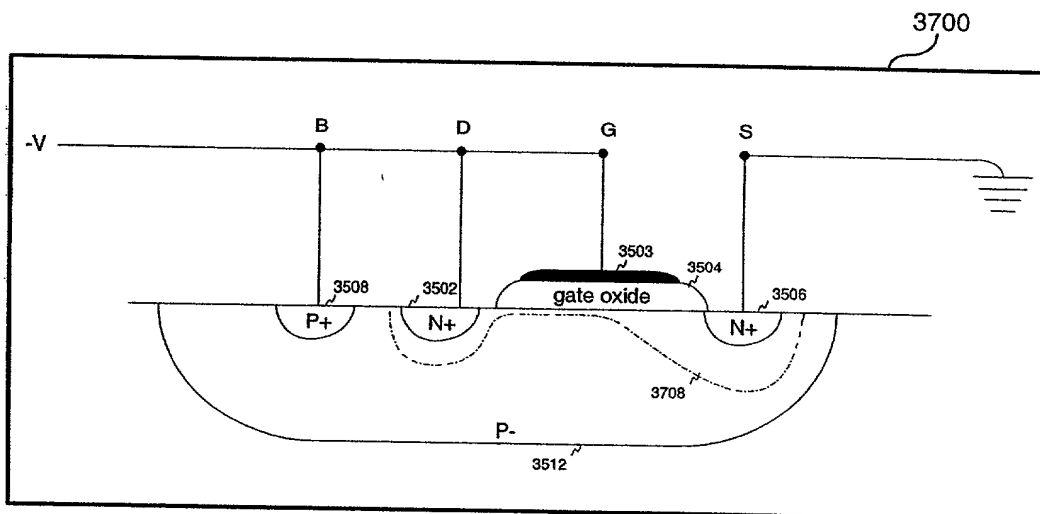


FIG. 37C

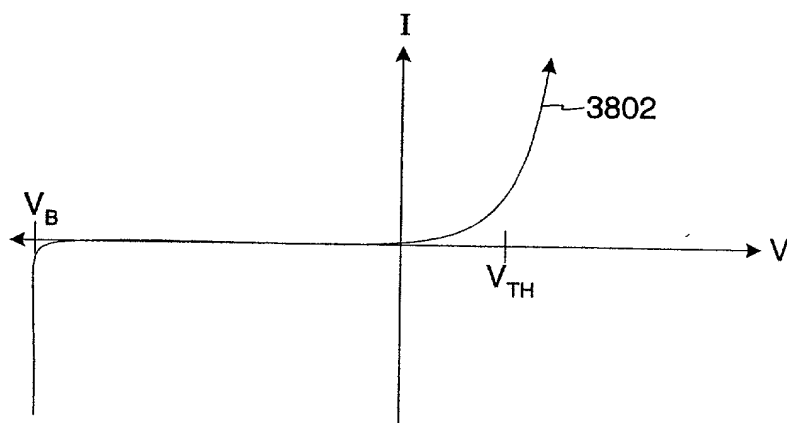


FIG. 38A

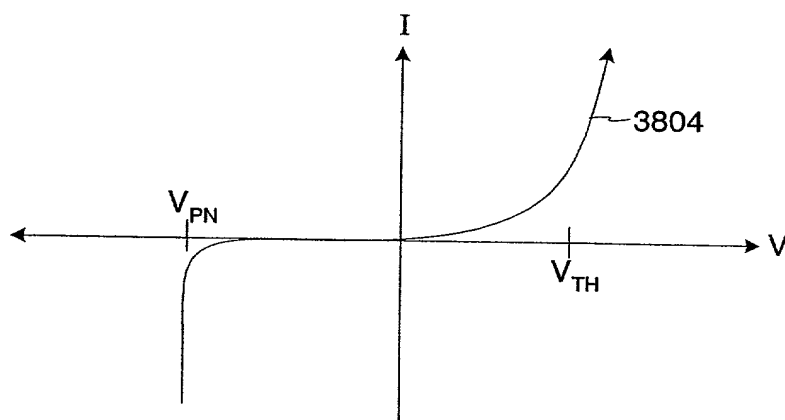


FIG. 38B

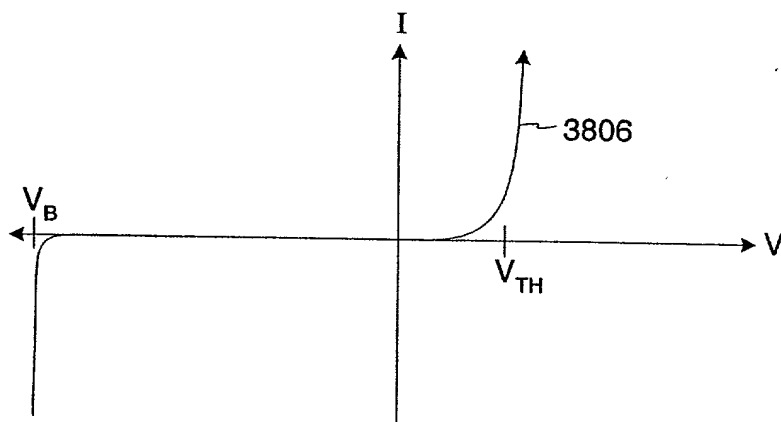


FIG. 38C

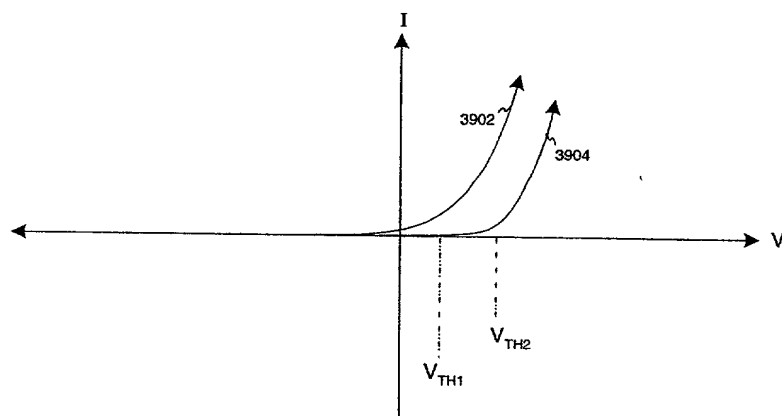


FIG. 39

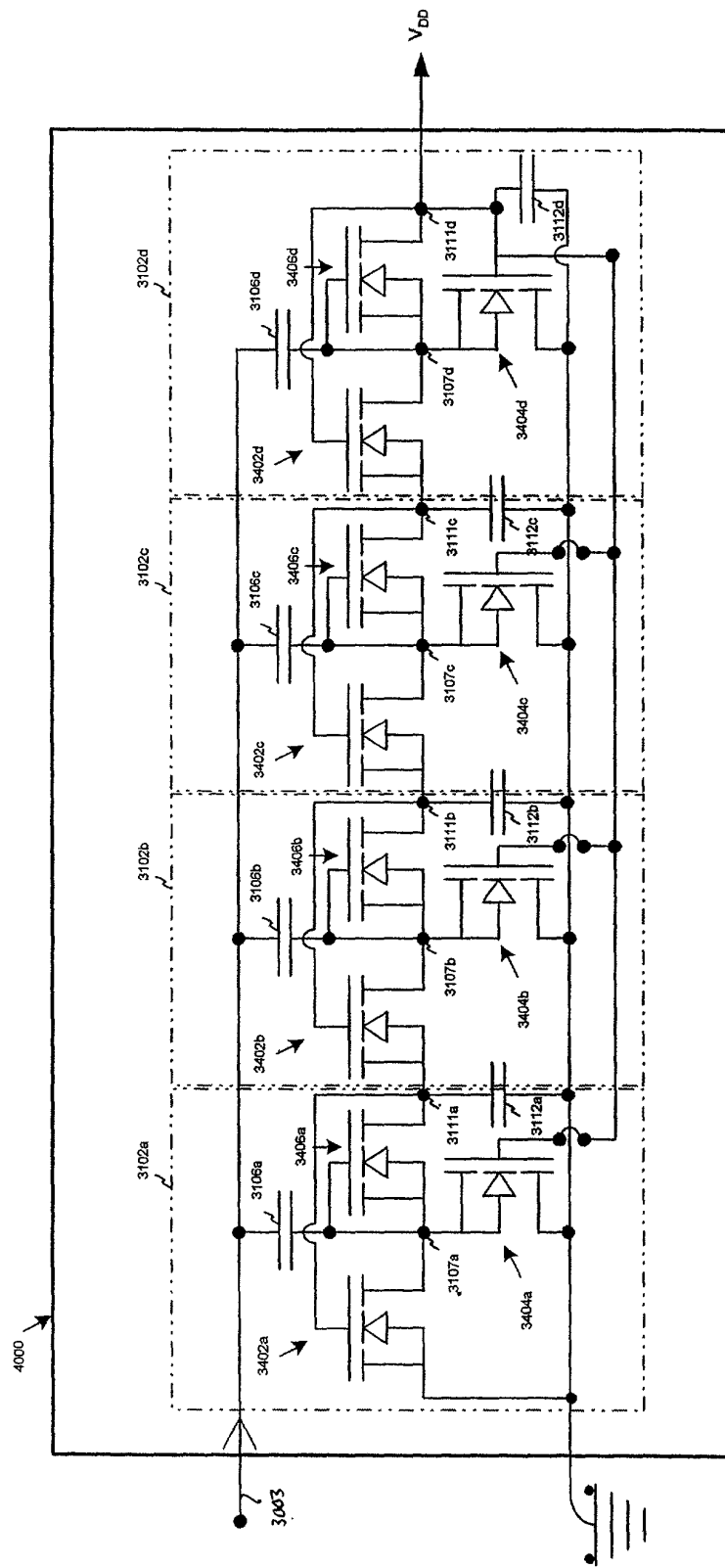


FIG. 40A

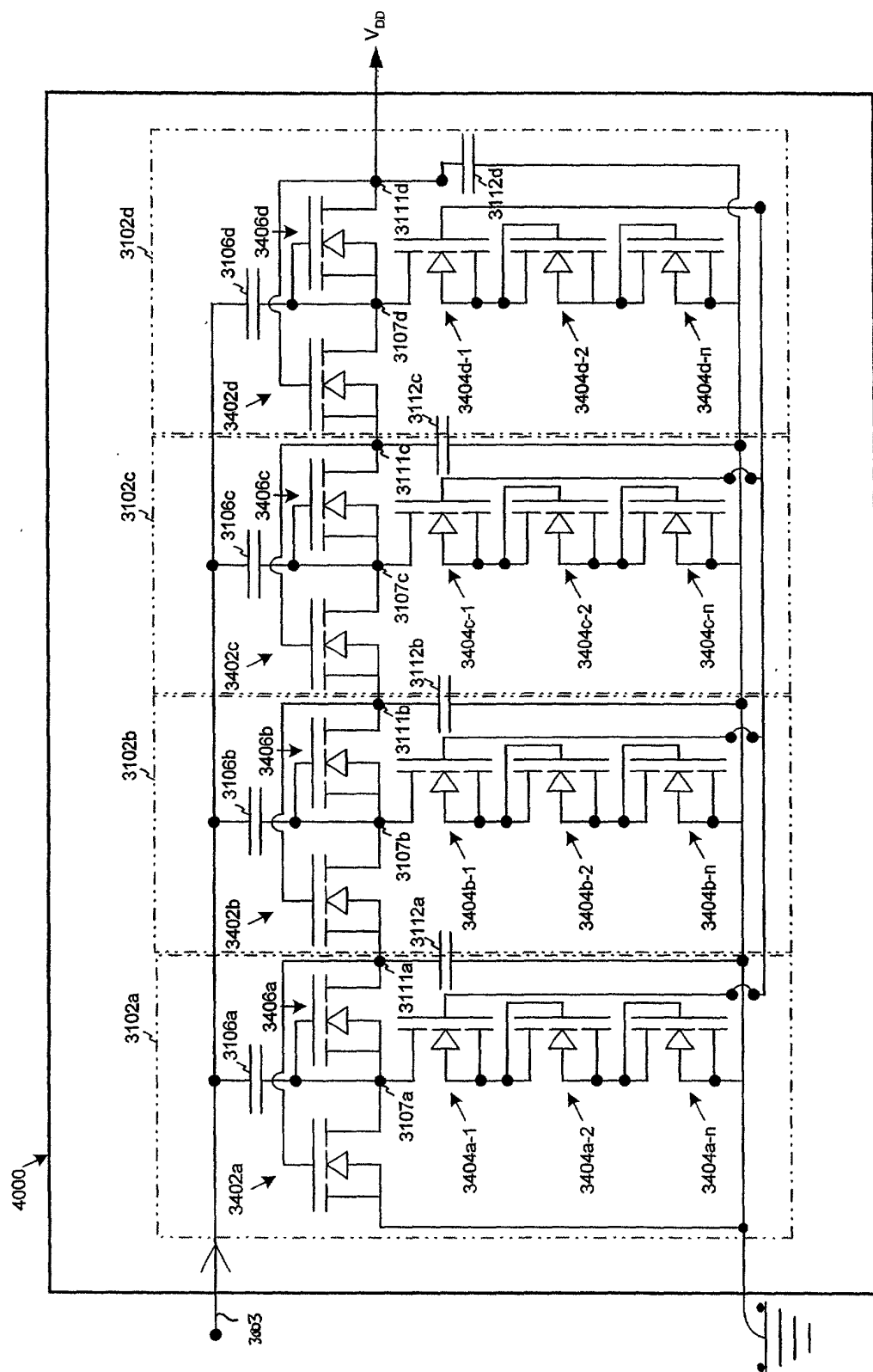


FIG. 40B

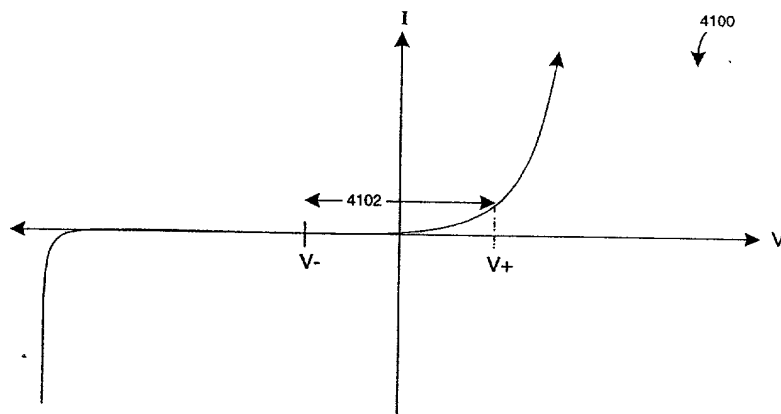


FIG. 41A

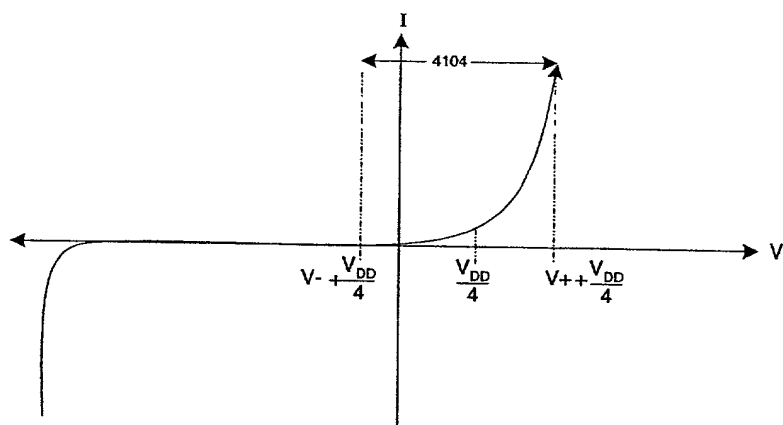


FIG. 41B

2025-08-08 00:00:00

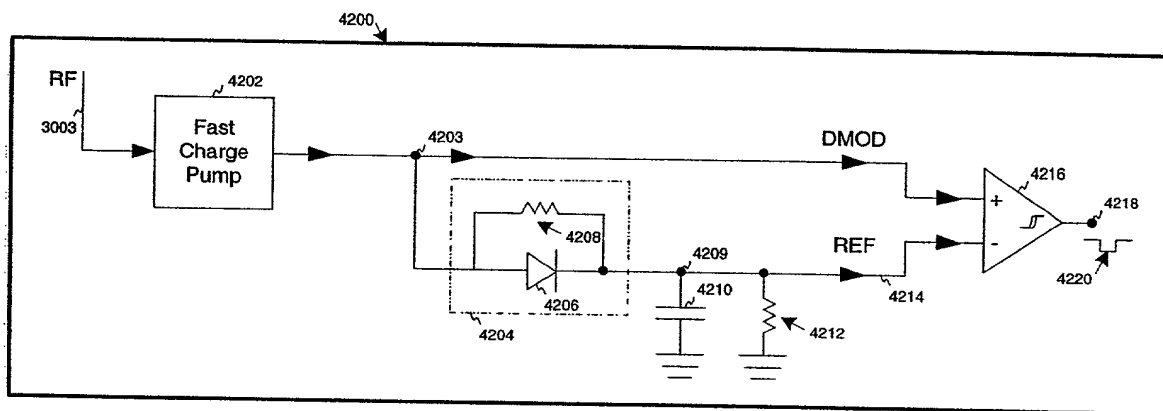


FIG. 42

FIG. 4 is a cross-sectional view of a device. It shows a substrate with a top layer 4203, a middle layer 4302, and a bottom layer 4214. A dashed line indicates a boundary or interface between the middle and bottom layers.

FIG. 43B

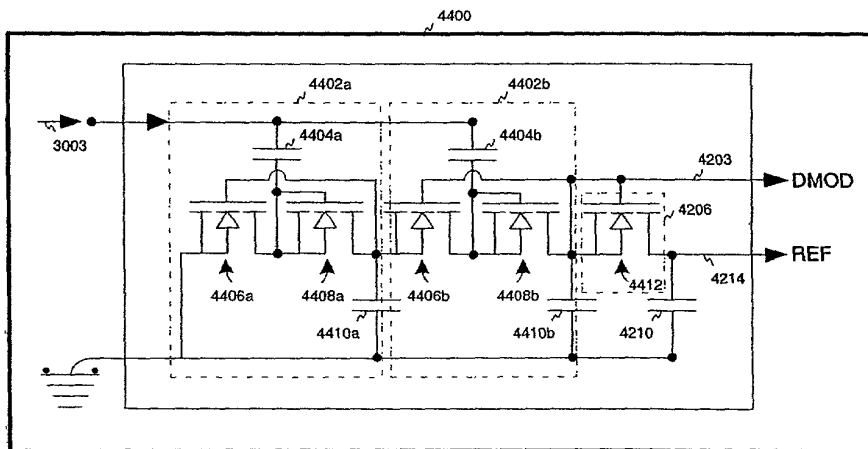


FIG. 44

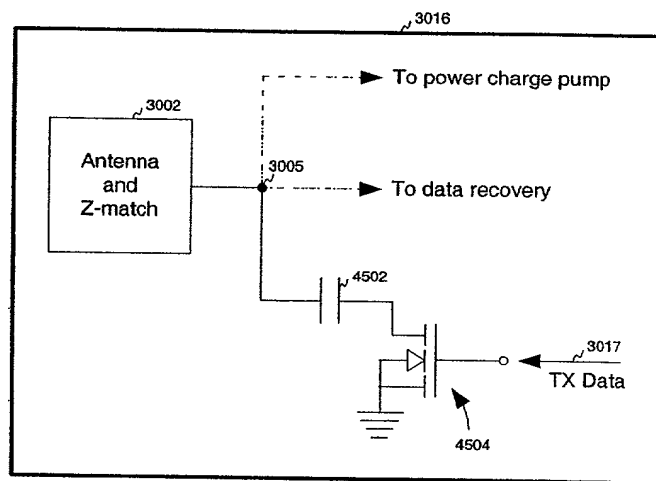


FIG. 45